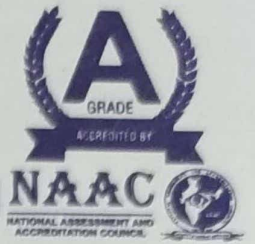




Bharath

INSTITUTE OF HIGHER EDUCATION AND RESEARCH

(Declared as Deemed-to-be-university under section 3 of UGC Act 1956)



BHARATH INSTITUTE OF SCIENCE AND TECHNOLOGY

173, Agaram Road, Selaiyur, Chennai - 600 073. Tamil Nadu, India.

Name ... G. SINDHU
Course ... B.TECH. Branch ... CSE.
Year ... 1st Semester ... I

Register No.

U19CS387

Certified to be the bonafide Record of work done by the above student in the
U18ESEEL3. Basic Electrical and Electronic Engineering laboratory during the
..... 1st Semester in the Academic Year 2019-2020

Signature of the Lab-in-charge

Hailma
16/11/19

Signature of the Head of Dept



Submitted for the practical examination held on ... 30.11.19

Internal Examiner

Hailma

External Examiner

P. Ash

INSTRUCTIONS FOR MAINTAINING THE RECORD NOTE BOOK

1. The Record should be written neatly in ink on the pages of the right handside and the diagrams / drawings to be drawn on the pages of the left hand side in pencil.
2. Every Experiment should begin on a new page.
3. The right hand side pages of the record should contain :
 - I. Sl. No. and date of performance of the Experiment in the margin at the top
 - ii. Experiment No. and the title of the Experiment on the first line followed by
 - iii. Aim of the Experiment
 - iv. A list of apparatus required
 - v. Description of the apparatus.
 - vi. Theory of the Experiment in brief
 - vii. Inference of the result.
4. The left hand side pages of the Record should contain :
 - I. Neat sketches of apparatus used and full page graphs wherever possible.
 - ii. Diagrams of Electrical connections neatly drawn,
 - iii. Observation (to be entered in a tabular form neatly wherever possible)
 - iv. A detailed account of manipulation.

Electrical

CONTENTS

SL. No.	DATE	NAME OF THE EXPERIMENT	PAGE No.	MARKS	REMARKS
1.	9-8-19	Flourescent Lamp Wiring	1-2	10	10/10
2.	6-9-19	stairs case wiring	3-4	10	10/10
3.	20-9-19	Measurement of energy using single phase energy meter	5-6 5-6	10	10/10
4.	11-10-19	verification of ohm's law	7-8	10	10/10
5.	1-11-19	Verification of kirchoff's voltage law	9	10	10/10
6.	15-11-19	verification of kirchoff's current law	10	10	10/10

Completed
15/11/19

✓

Electronics

CONTENTS

SL. No.	DATE	NAME OF THE EXPERIMENT	PAGE No.	MARKS	REMARKS
1.	2-8-19	Verification of logic Gates	1-2	10	A
2.	30-8-19	Measurement of signal parameters using cathode ray oscilloscope	4-5	10	A
3.	13-9-19	Verification of flip flops	6-7	10	A
4.	29-9-19	Verification of half wave and full wave rectifiers	8-11	10	A
5.	18-10-19	Applications of OP-AMP	12-14	10	A
6.	8-11-19	Characteristics of BJT in CB configuration	15-17	10	A

Completed
 Hailma
 10/11/19

AIM:-

To prepare wiring for a fluorescent tube light with switch control.

TOOLS required:-

1. Screw driver
2. Hammer
3. Pliers
4. Line tester

Components required:-

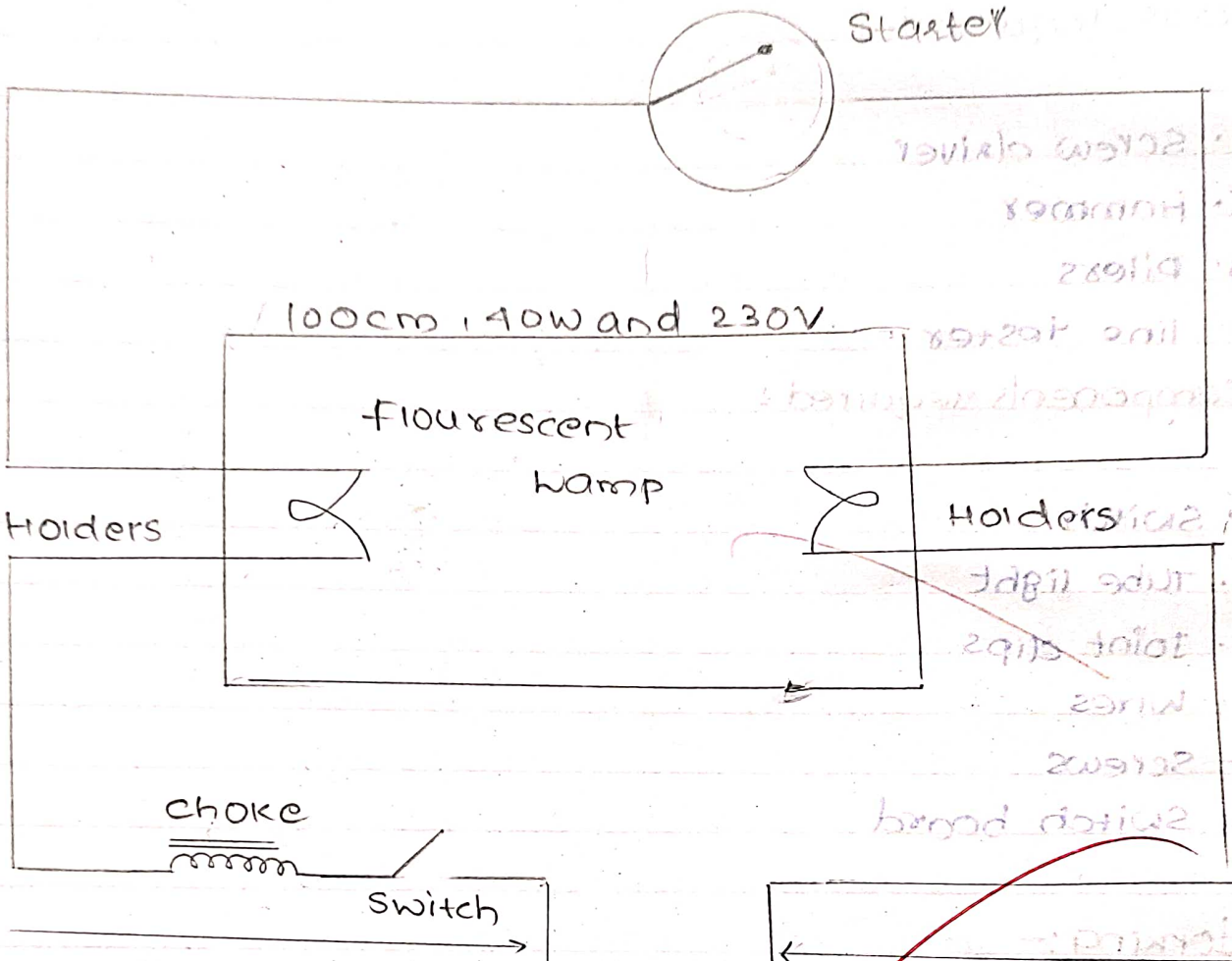
1. Switch
2. Tube light
3. Joint clips
4. Wires
5. Screws
6. Switch board

Working:-

The fluorescent lamp consist of a choke, a starter, a fluorescent tube and a frame. The light of the commonly used fluorescent tube is 100cm its power rating is 40w and 230V. The tube is filled with argon and a drop of Mercury. When the supply is switched on, the current heats the filaments and initiates, the emission of electrons. After one or two seconds, the starter circuit opens and makes the choke to introduce a momentary high voltage surge across the two filaments.

Circuit diagram - Tube light :-

P-2-19



$V = 230 \angle \phi$
 $Z = 50 \angle \theta$
 Phase Neutral

The fluorescent lamp consists of a circular glass tube and a frame. The tube is 100cm long and contains a mixture of mercury and argon. The tube is fitted with a drop of mercury. When the supply is switched on, the current heats the filament and ionizes the gas. The starter circuit opens after one or two seconds, the starter produces a momentary high voltage surge across the two filaments.

Ionisation takes place through argon and produce bright light.

Procedure:-

1. Mark the switch and tube light location points and draw lines for wiring on the wooden board.
2. place wires along the lines and fix them with the help of clips.
3. fix the switch and tube light fitting in the Marked position.
4. Complete the wiring as per the wiring diagram.
5. Test the working of the tube light by giving electric supply to the circuit.

Result!

RESULT:-

The wiring for the tube light is completed and tested.

Aim:

To wire for stairs case arrangement using a two way switch.

Tools Required:

1. Screw driver
2. Hammer
3. Pliers
4. Line tester

Components required:

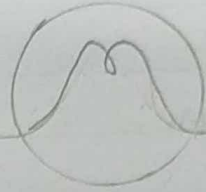
1. Two way switch
2. Bulb holder
3. Bulbs
4. Joint clips
5. Wires
6. Screws
7. Ceiling rose
8. Switch board.

Procedure:

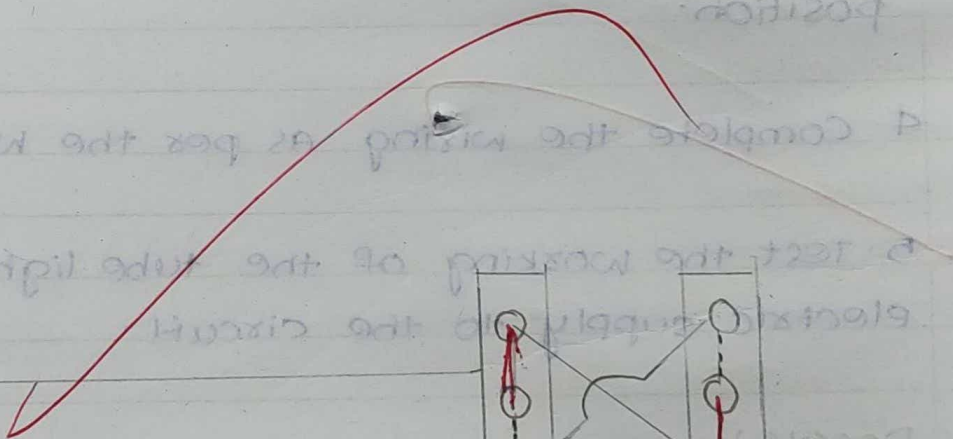
1. Make the bulb and bulb location points and drawn lines for wiring on the wooden board.
2. Place wire along the line and fix them with the help of clips.
3. Fix the two way switches and bulb holder in the marked position on the wooden board.
4. Complete the wiring as per the wiring diagram.
5. Test the working of the bulb by giving electric supply to the circuit.

Circuit diagram:-

60 watt Lamp



10, 230V
50Hz AC SUPPLY



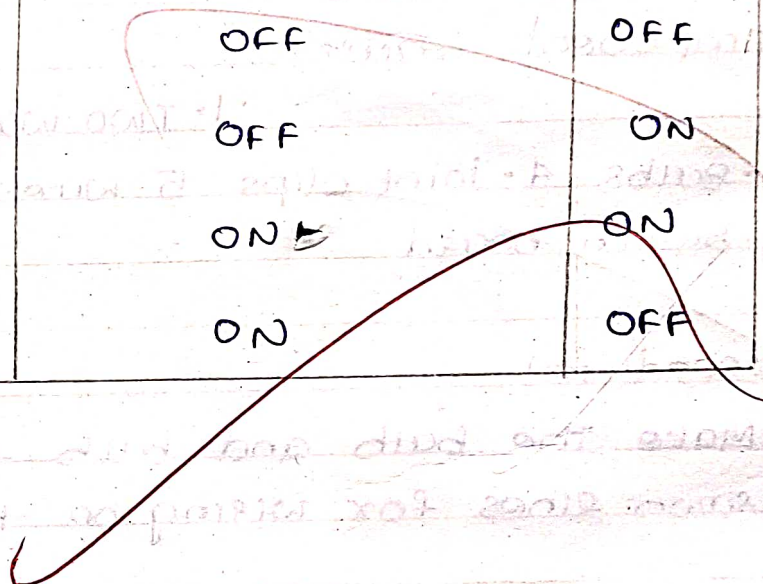
Two way
Switch - I

Two way
Switch - II

~~The wiring for the tube light is completed and tested.~~

Tabulation:

Switch position		Lamp condition
Switch - 1	Switch - 2	
OFF	OFF	OFF
ON	OFF	ON
OFF	ON	ON
ON	ON	OFF



Theory:

A two way switch is installed near the first step of the stairs, the other two way switch is installed at the upper part, where the stairs the light point is provided b/w first and last stairs at an aqoes adequate location and height. If the lower switch switches on the light, the switch at the top or vice versa on switch. It off two numbers of two way switches are used. for the purpose the supply is given to the switch at the short circuited terminals the for the second switch other two terminals of each circuit are connected through cables.

~~Result~~ Result: The stair case wiring is completed and tested.

Aim :- To measure the energy consumed in a single phase circuit using energy metre.

Apparatus required :-

S.No	components required	Range	TYPE	Quality
1.	Ammeter	0 to 10 Amp	MI	1
2.	Load	-----	(AMP)	-----
3.	voltmeter	0 - 30V	MI	1
4.	Energy meter	1 Ph, 300V, 10A	-	1
5.	Auto transformation (0-240)	1 kVA, 230	± PH	1

Formula :-

$$1200 \text{ Rev} = 1 \text{ kW}$$

$$1 \text{ Rev} = 1 \times 1000 \times 3000 / 1200 = 3000 \text{ (watt/sec)}$$

$$\text{For } N \text{ Rev indicated energy } (E_3) = N \times 3000 \text{ (watts)}$$

$$\% \text{ Error} = (E_3 - E_0) \times 100 / E_3$$

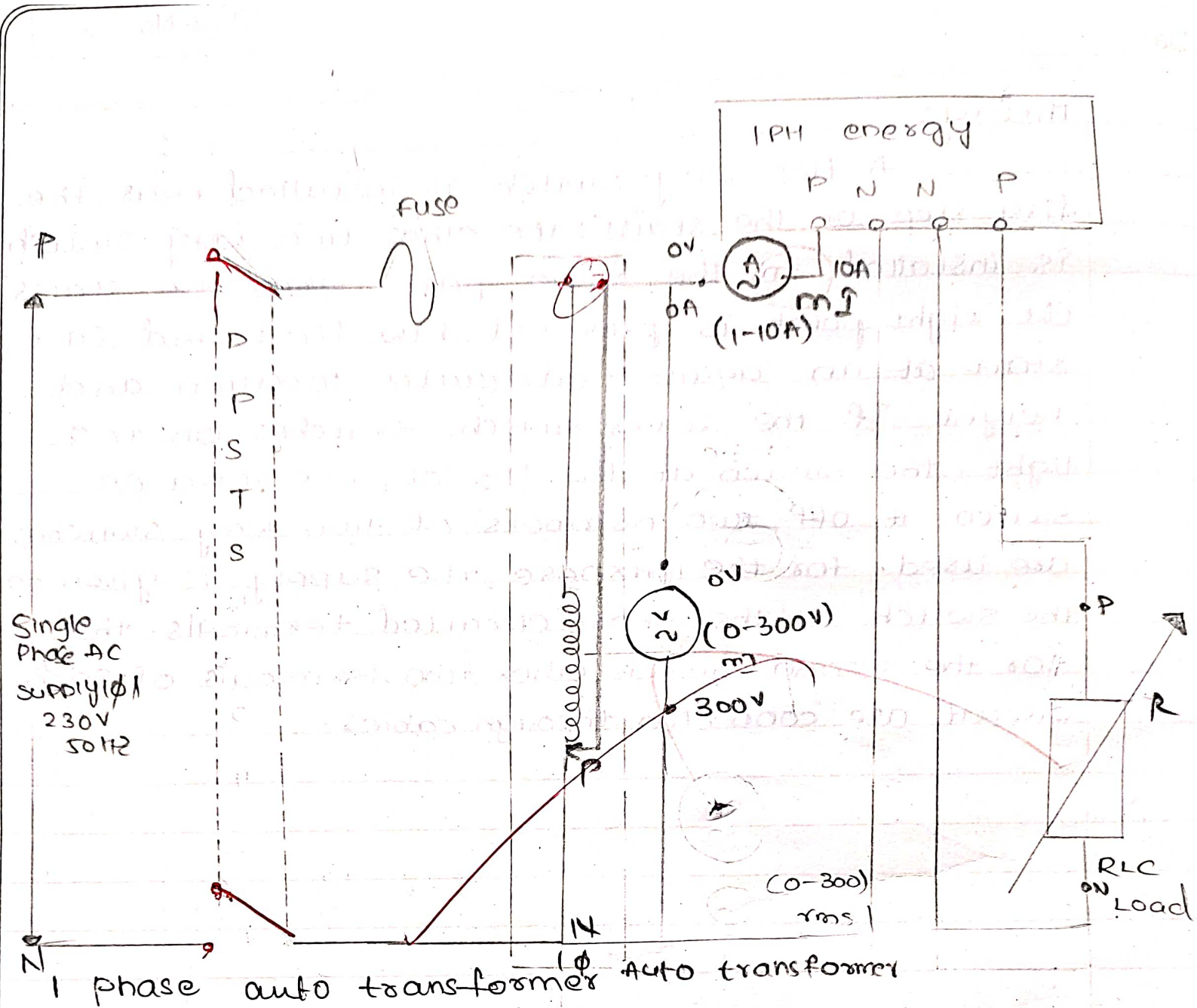
$$\text{Calculated energy } E_0 = (V_L \times I_L) \times T \text{ (watt/sec)}$$

where,

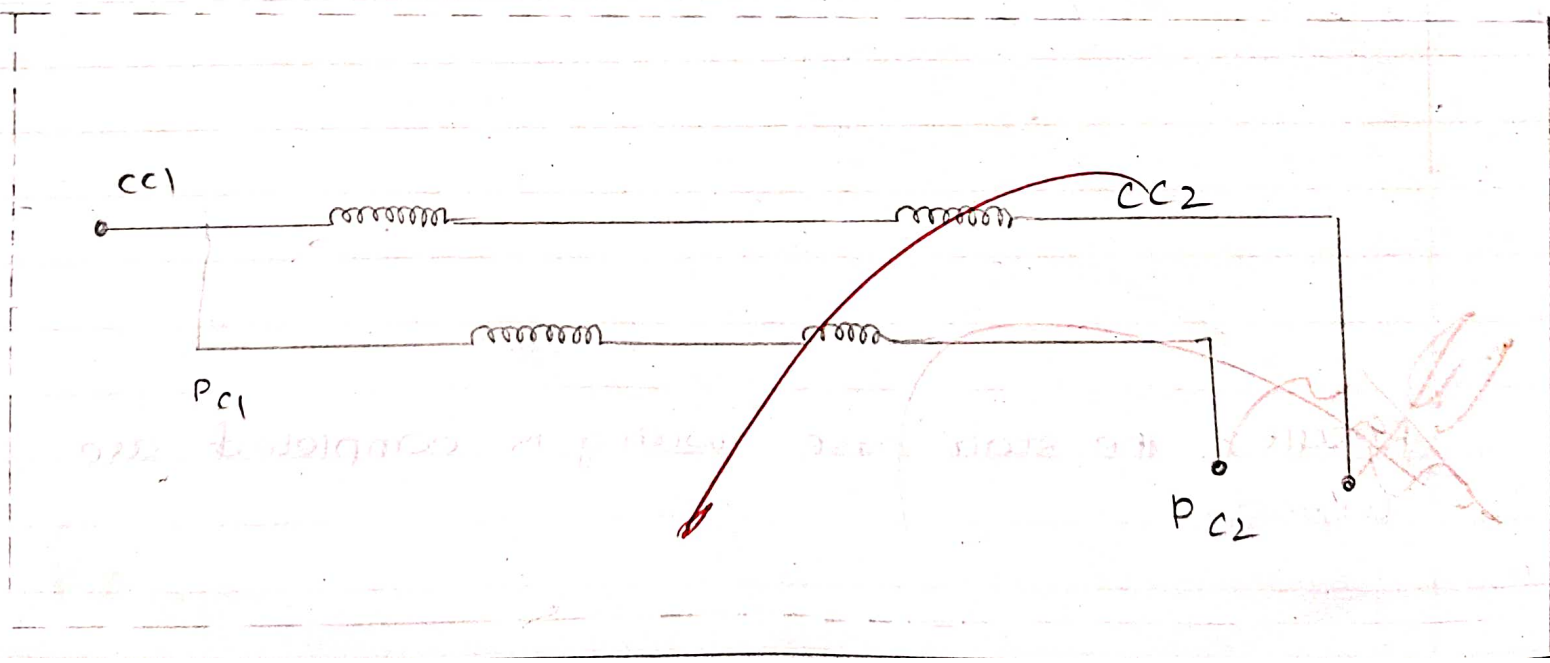
$$V_L = \text{Load voltage}$$

$$I_L = \text{Load current}$$

$$\text{Energy meter constant} = 5 \text{ Rev/sec}$$



Energy meter internal connection :-



Tabulation :-

S.No	voltmeter reading (volts)	ammeter reading (Amps)	time taken for 5 revs (sec)	calculated energy (sec)	Indicated Energy
1.	220	0	250	0	183000
2.	220	1.3	1120	34320	183000
3.	220	2.04	65	34320	183000
4.	220	2.5	60	33000	183000

$$\% \text{ error} = \frac{E_i - E_a}{E_i} \times 100$$

$$1) \% \text{ error} = \frac{E_i \times 100}{E_i} = 100.1$$

$$2) \% \text{ error} = \frac{E_i - E_a}{E_i} \times 100 = 81.24.1$$

$$3) = \frac{148680}{183000} \times 100$$

$$\% \text{ error} = \frac{E_i - E_a}{E_i} \times 100 = \frac{148680}{183000} \times 100 = 81.24.1$$

$$4) \% \text{ error} = \frac{E_i - E_a}{E_i} \times 100 = \frac{183000 - 33000}{183000} \times 100 = 81.96.1$$

procedure:-

1. connections are made as per circuit diagram.
2. supply is switch on and load is applied and ammeter voltmeter reading and note time taken by the disc for particular number of revolution are noted using stop watch.
3. step two is repeated for various load condition.
4. % error is calculated.

Result:-

The energy consumed in a single phase circuit is measured.

Date: 11-10-19

VERIFICATION OF OHM'S LAW

Dirn:-

To verify ohm's law for the given circuit.

Apparatus Required:-

S.NO	Apparatus	Range	Quantity
1.	RPS (regulated power supply)	(0-30) V	1
2.	Resistance	230Ω, 200Ω, 1KΩ	Each 1
3.	Ammeter	(0-30mA) mc	1
4.	Voltmeter	(0-30) vmc	1
5.	Bread board wires	-	Required

Statement:-

At constant current temperature current flow through a conductor is directly proportional to the potential difference between the two ends of the conductor.

$$I \propto V$$

$$V = IR$$

procedure.

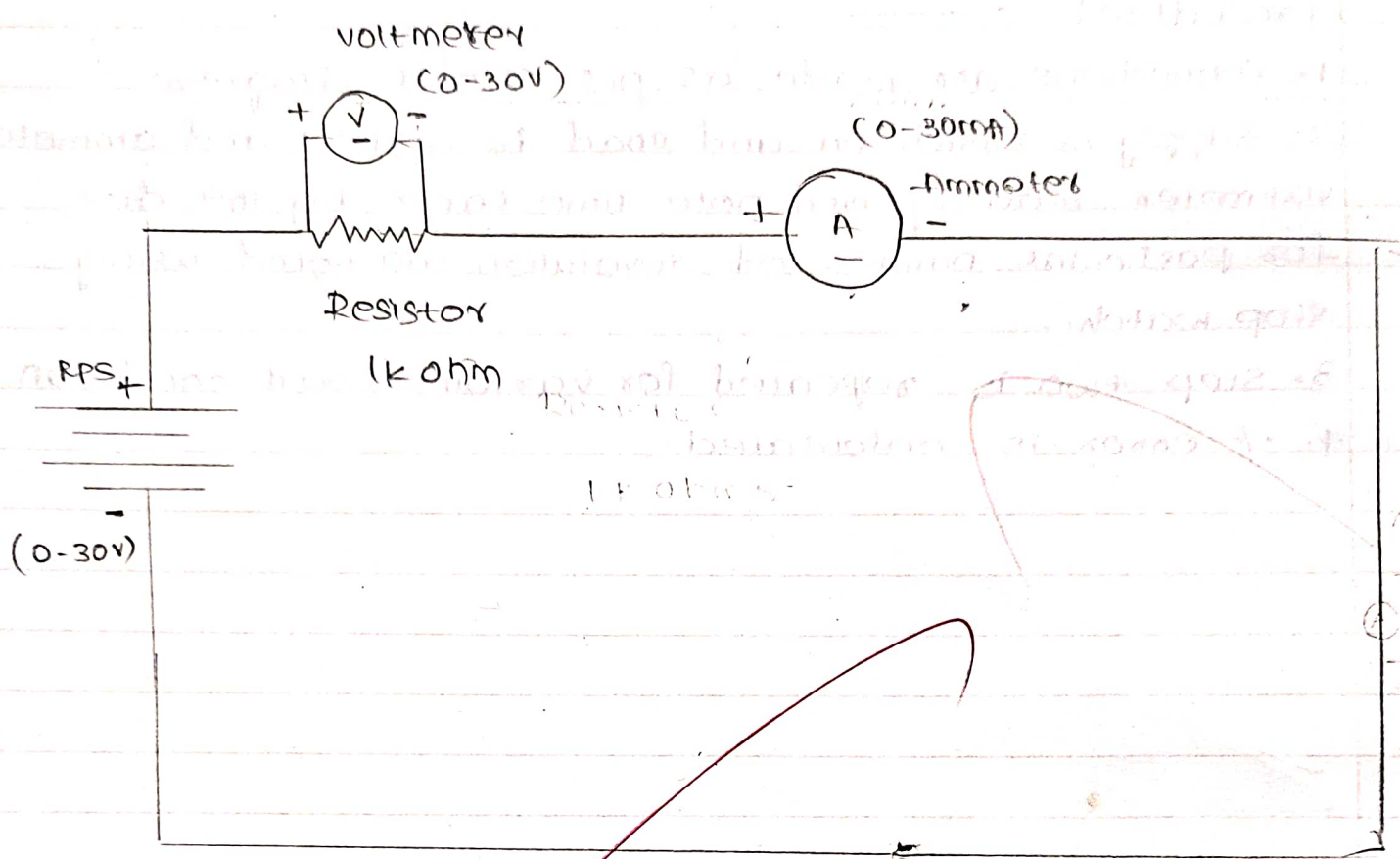
=> Draw the circuit diagram as shown above.

=> Arrange the apparatus as per the circuit diagram

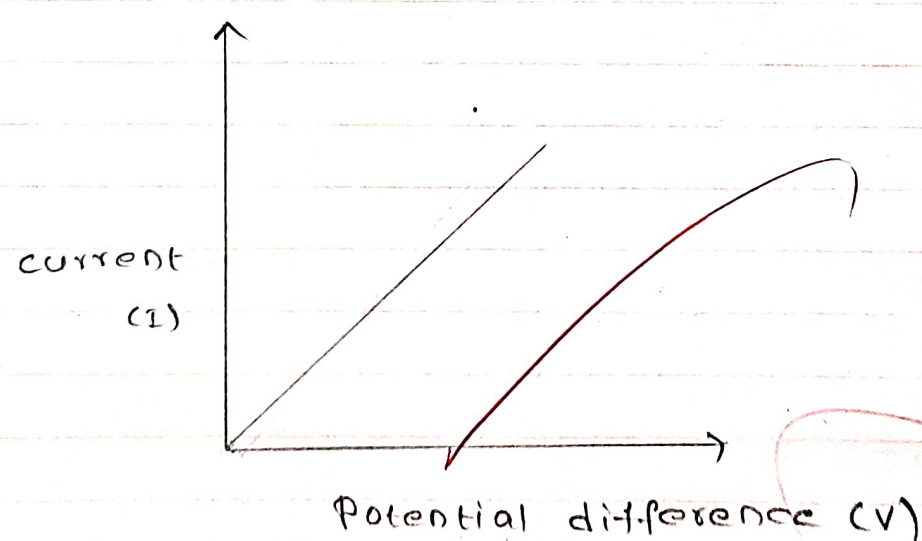
=> Make the connections as per the circuit diagram

All connections must be neat and tight. Take care

to connect the ammeter and voltmeter with their correct polarity. (+ve to +ve) and (-ve to -ve)



Model diagram ←



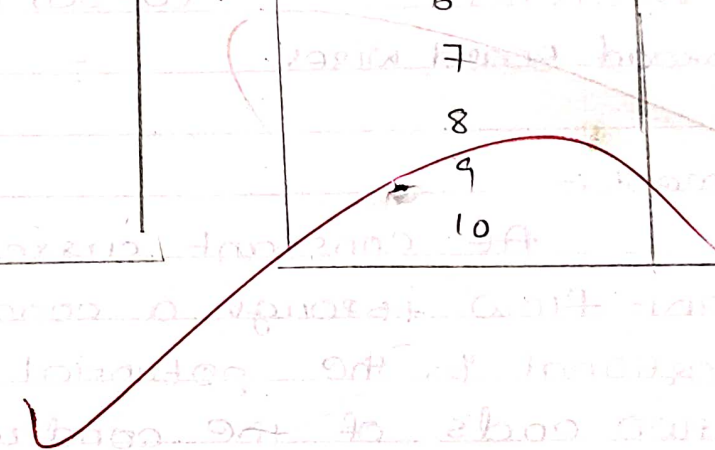
[Handwritten signature]

Theoretical value

Practical value

voltage (v)	current (A)
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10

voltage (v)	current (A)
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10



⇒ Determine the zero error and least count of the ammeter and voltmeter and record them.

⇒ Adjust the RPS to get a small deflection in ammeter and voltmeter.

⇒ Record the recordings of ammeter and voltmeter.

⇒ Take at least sets of readings by adjusting the RPS gradually.

⇒ Plot a graph with V along x -axis and I along y -axis.

⇒ The graph will be straight line which verifies

⇒ Determine the slope of the $V-I$ graph. The reciprocal of the slope gives resistance of the wire.

Result :-

~~Thus Ohm's law is verified both practically and theoretically.~~

Expt. No. 5
Date: 1-11-19

VERIFICATION OF KIRCHOFF'S
VOLTAGE LAW

Page No. 9

Aim:

To verify kirchoff's voltage law for the given circuit

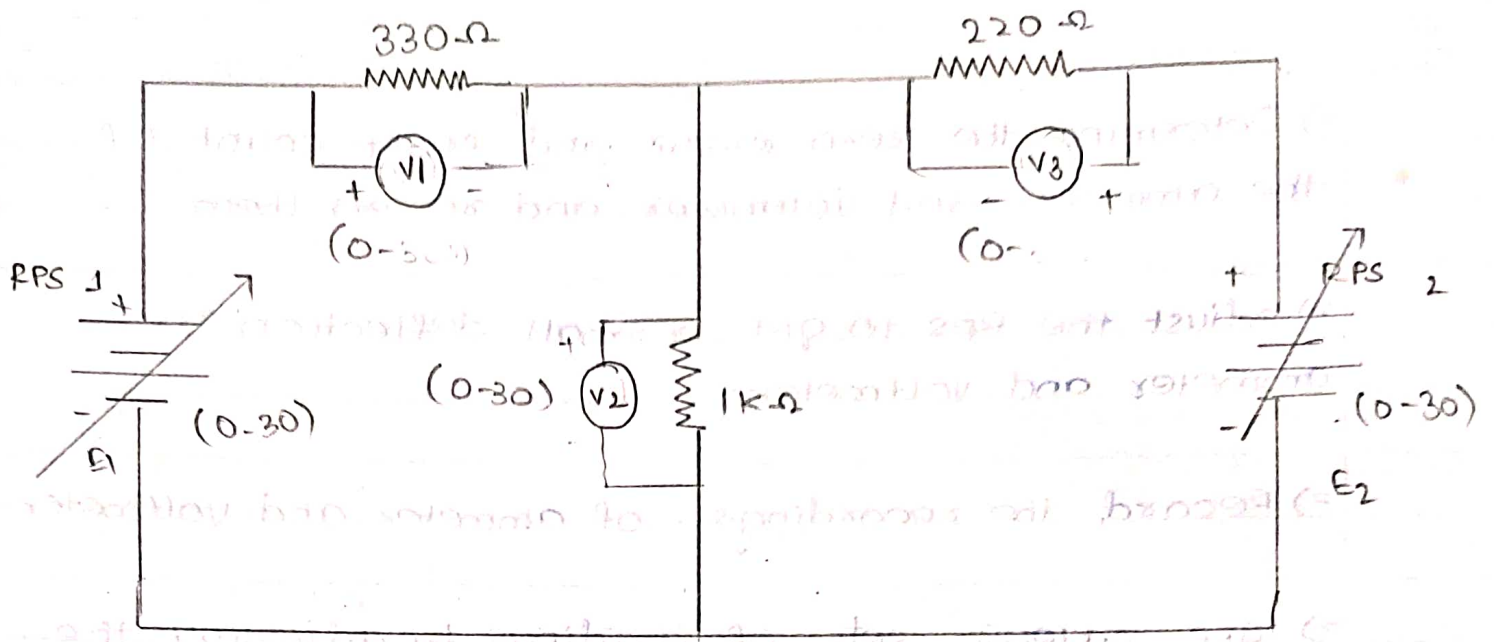
Apparatus required :-

S.NO	APPARATUS	Range	Quantity
1.	Rps (Regulated power supply)	(0-30V)	2
2.	Resistance	330 Ω , 220 Ω , 1K Ω	6
3.	Ammeter	(0-30mA) MC	3
4.	Voltmeter	(0-30V) MC	3
5.	Bread Board & wires	-	Required

Statement:

KVL: In any closed path/mesh, the algebraic sum of all the voltages is zero.

~~Result~~ Thus kirchoff's voltage law is verified both practically and theoretically.



KVL - Theoretical values

S.NO	RPS		Voltage			KVL $E_1 = V_1 + V_2$
	E_1	E_2	V_1	V_2	V_3	
	V	V	V	V	V	V
1	5	5	0.58	4.41	0.583	4.99
2	10	10	1.16	8.83	1.17	9.99
3	15	15	1.75	13.2	1.75	14.95
4	20	20	2.33	17.67	2.33	20
5	25	25	2.913	22.08	2.915	24.993

KVL - Practical values

S.NO	RPS		Voltage			KVL $E_1 = V_1 + V_2$
	E_1	E_2	V_1	V_2	V_3	
	V	V	V	V	V	V
1	5	5	0.6	4.4	0.56	5
2	10	10	1.13	8.83	1.19	9.96
3	15	15	1.72	13.20	1.78	14.92

Date: 15-11-19

LAW

Aim: To verify kirchoff's current law for given circuit

Apparatus Required:

S.No	APPARATUS	Range	Quantity
1.	RPS (Regulated power supply)	(0-30V)	2
2.	Resistance	330- Ω , 220- Ω , 1k- Ω	6
3.	Ammeter	(0-30 mA) MC	3
4.	voltmeter	(0-30V) MC	3
5.	Bread Board & wires	-	Required

Statement:

KCL: The algebraic sum of the currents meeting at a node is equal to zero.

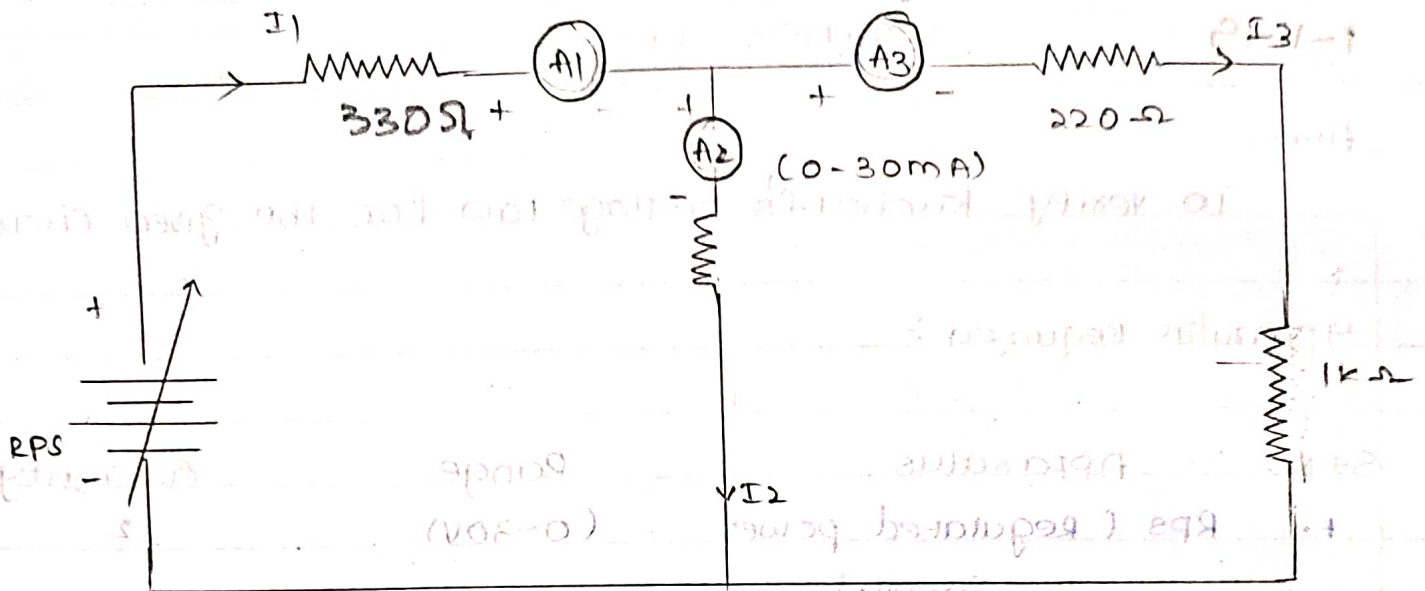
precautions:

1. Voltage control knob should be kept at minimum position.
2. Current control knob of RPS should be kept at Max position.

procedure for KCL:

1. Give the connections as per the circuit diagram.
2. set a particular value in RPS.
3. Note down the corresponding ammeter reading.
4. Repeat the same for different voltages.

Result: Thus kirchoff's current law is verified both practically and theoretically.



KCL - Theoretical value

S.NO	Voltage		Current			$i_1 = i_2 + i_3$
	E	E (Ammeter)	i_1	i_2	i_3	
	Volts	mA	mA	mA	mA	mA
1	5	5.68	3.12	2.56	5.68	
2	10	11.3	6.18	5.12	11.3	
3	15	17.05	9.37	7.68	17.05	
4	20	22.73	12.49	10.24	22.73	
5	25	28.42	15.62	12.80	28.42	

KCL - Practical value

S.I NO	Voltage		Current			$i_1 = i_2 + i_3$
	E	i_1	i_2	i_3		
	Volts	mA	mA	mA	mA	
1	5	5.6	3.1	2.5	5.6	
2	15	17.2	9.9	7.3	17.2	
3	25	28	15.6	12.4	28.0	

Practically and theoretically this Kirchhoff's voltage law is verified both.

Date: 2-8-19

Aim:

To construct OR, AND, NOT verify its truth table

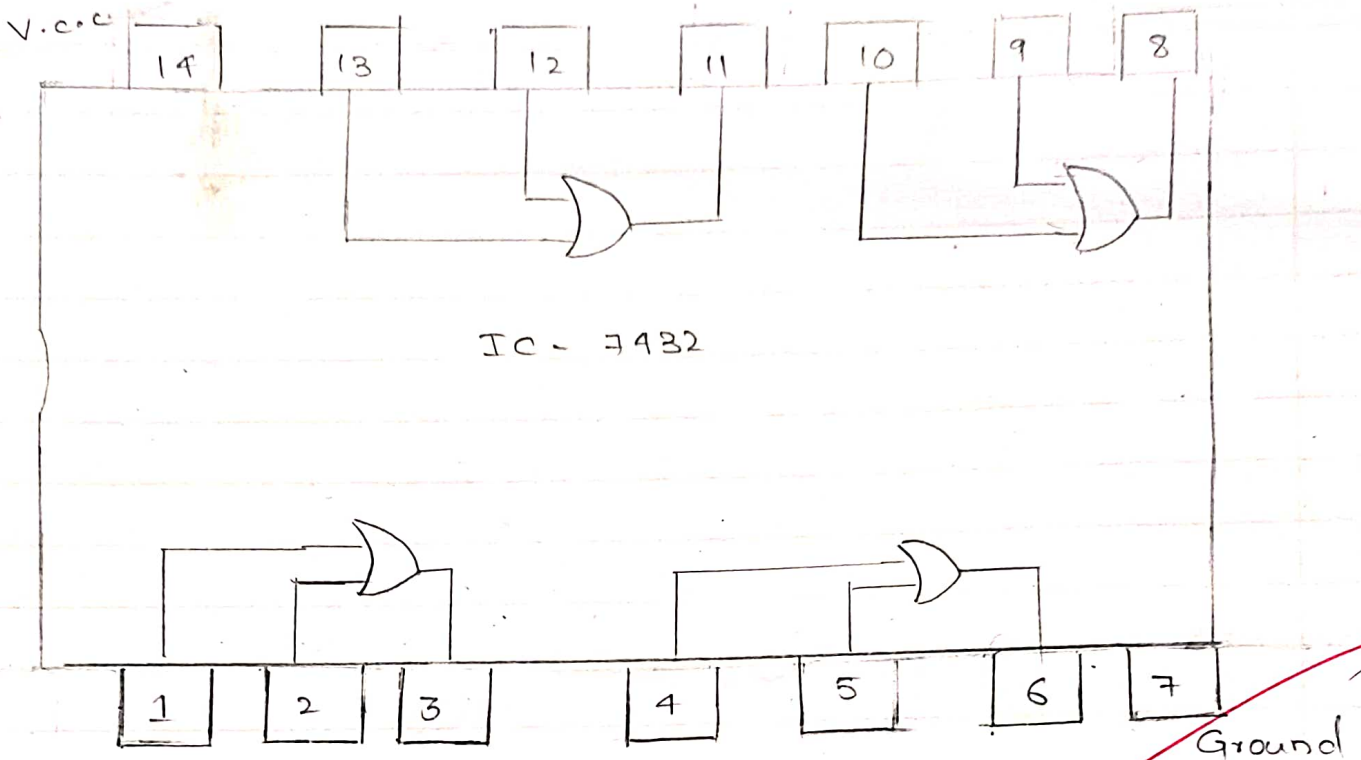
Apparatus required

1. OR GATE (IC-7432)
2. AND GATE (IC-7408)
3. NOT GATE (IC-7404)
4. NOR GATE (IC-7402)
5. NAND GATE (IC-7400)
6. EX-OR GATE (IC-7486)

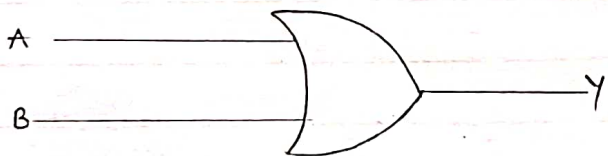
Procedure:

1. Connections are given as per the diagram.
2. The inputs A, B are given and the corresponding output is noted.
3. Observe the LED condition for if LED glows it represents logic 1. otherwise logic 0.
4. The truth table is verified from the given table.

IC PIN diagram of OR gate :-



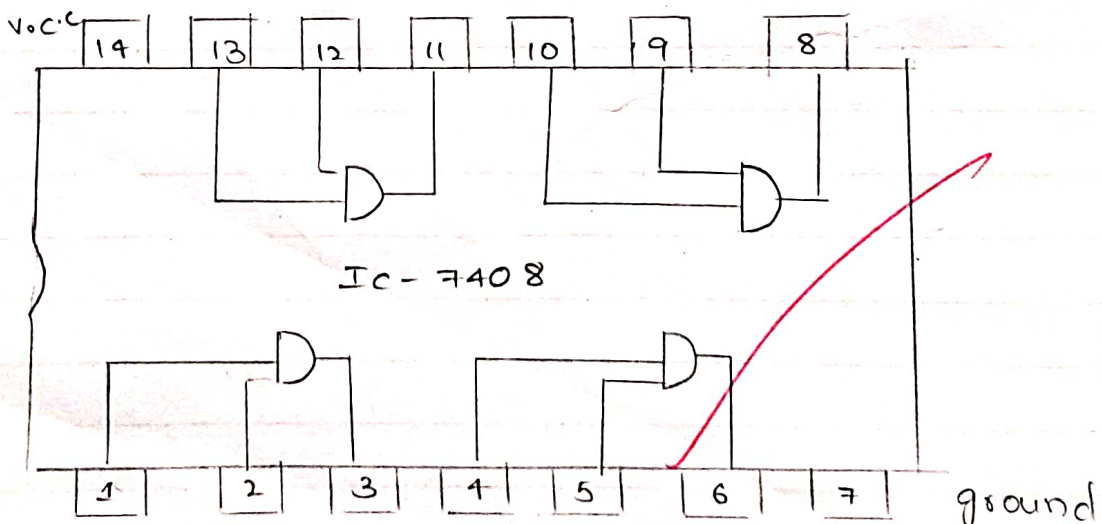
Logic gates diagram :-



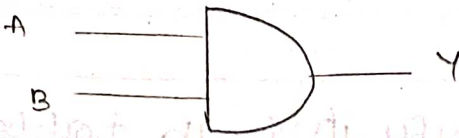
Truth Table

A	B	$Y = A + B$
0	0	0
0	1	1
1	0	1
1	1	1

IC PIN diagram of AND Gate :-



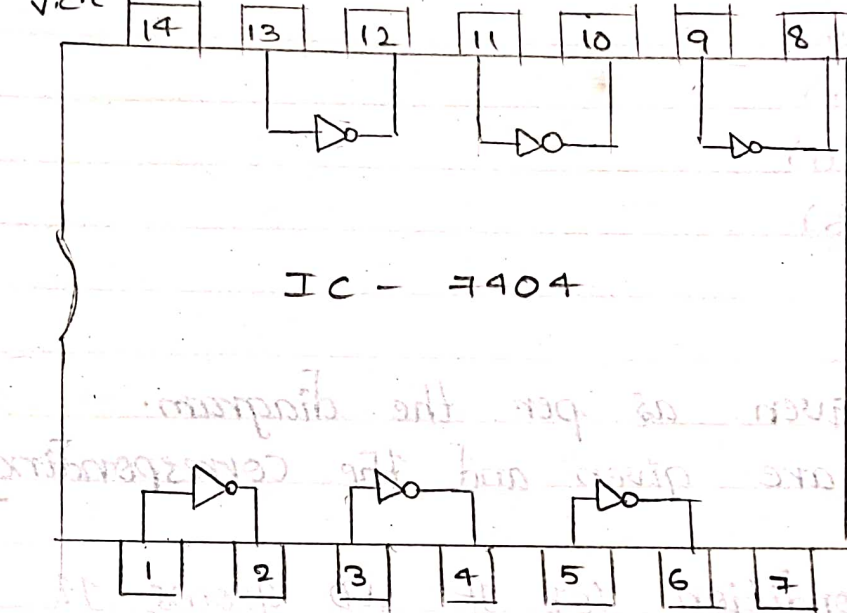
Logic gate diagram



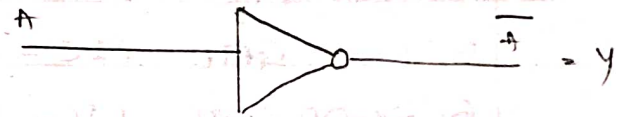
Truth Table

A	B	$Y = A + B$
0	0	0
0	1	0
1	0	0
1	1	1

IC PIN diagram of NOT GATE



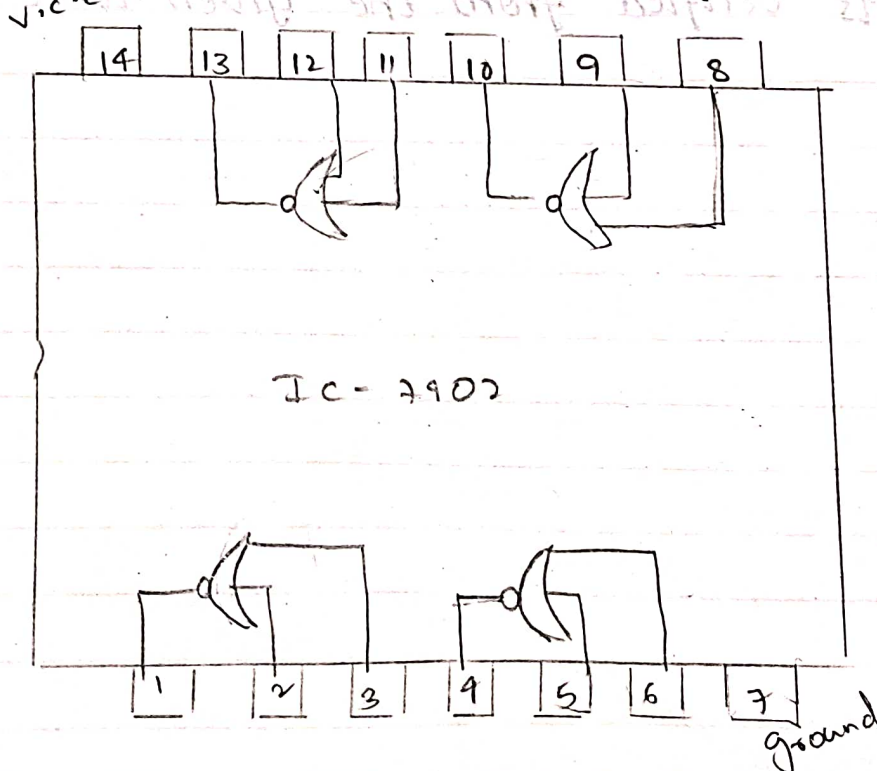
Logic Gate diagram



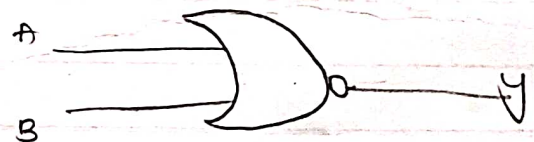
Truth table

A	$Y = \bar{A}$
0	1
1	0

IC PIN diagram of NOR gate



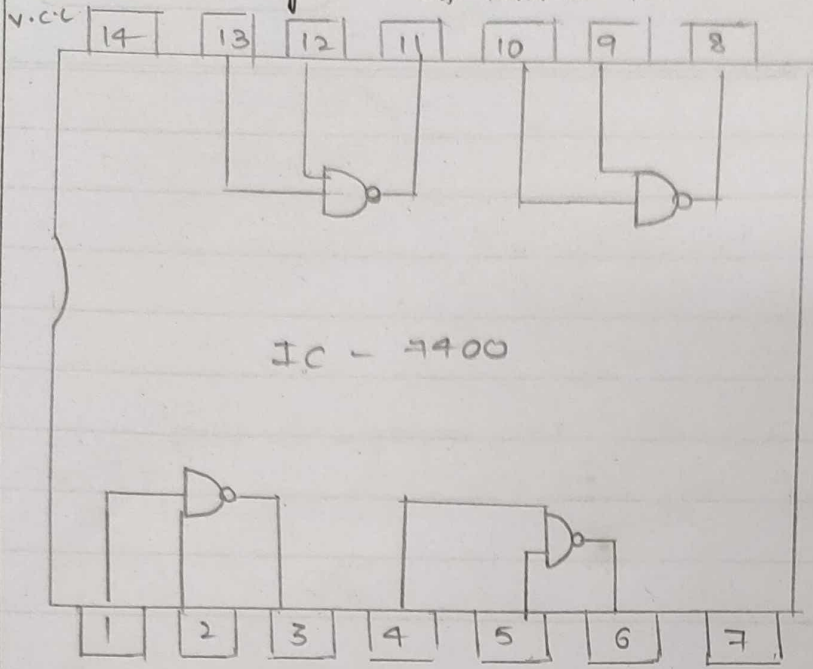
Logic gate diagram



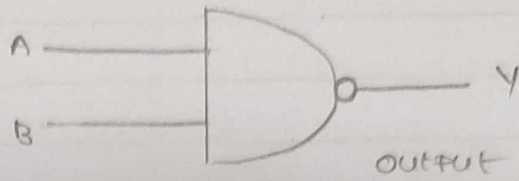
Truth table

A	B	$Y = \overline{A+B}$
0	0	1
0	1	0
1	0	0
1	1	0

IC PIN diagram of NAND Gate



logic gates diagram

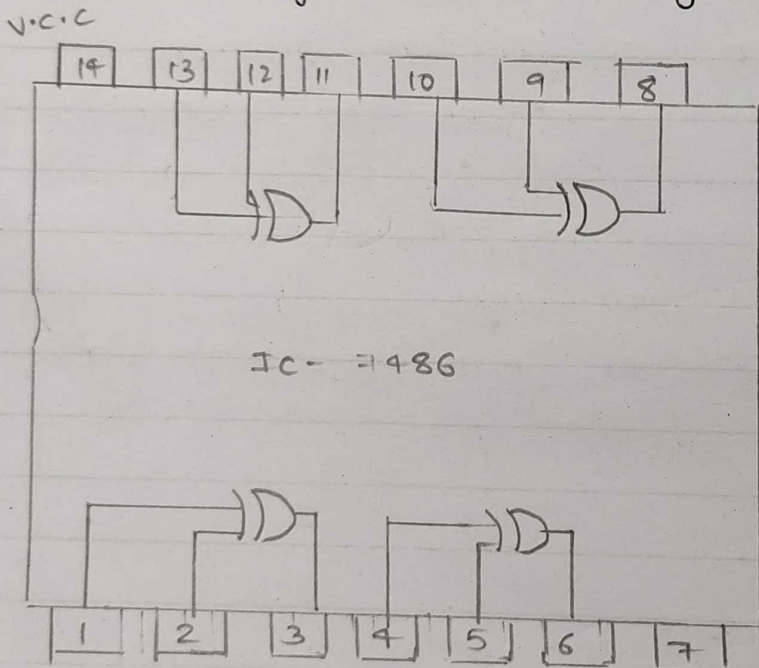


Truth table

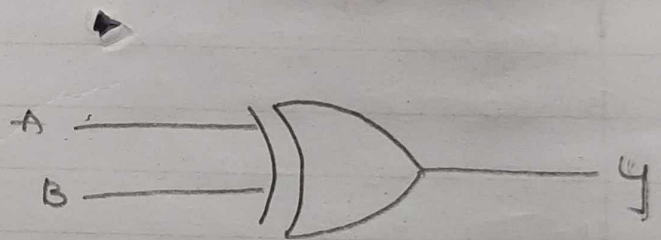
A	B	$Y = \overline{AB}$
0	0	1
0	1	1
1	0	1
1	1	0

ground

IC PIN diagram of Ex-OR gates :-



Logic gates diagram



ground

A	B	$Y = A \oplus B$
0	0	0
0	1	1
1	0	1
1	1	0

Result:

Thus, the OR, AND, NOT, NOR, NAND, EX-OR gate is tested and truth table has been verified.

Date: 30/8/19.

USING CATHODE RAY OSCILLOSCOPE

Aim:-

To measure the AC signal parameter using CRO

(i) voltage

(ii) Time period

(iii) frequency

Apparatus required :-

(i) CRO

(ii) function generator (3MHz)

Theory of CRO:-

Typical CRO consists of following control:-

(i) Intensity knob:-

It control the brightness of the track.

(ii) DC/AC switch:-

DC position provides direct coupling to the amplifier

AC provides capacitor in series input to block the DC component.

(iii) ON-OFF switch:-

Power ON/OFF switch

(iv) focus knob :-

It provides voltage control to the sharpness of trace.

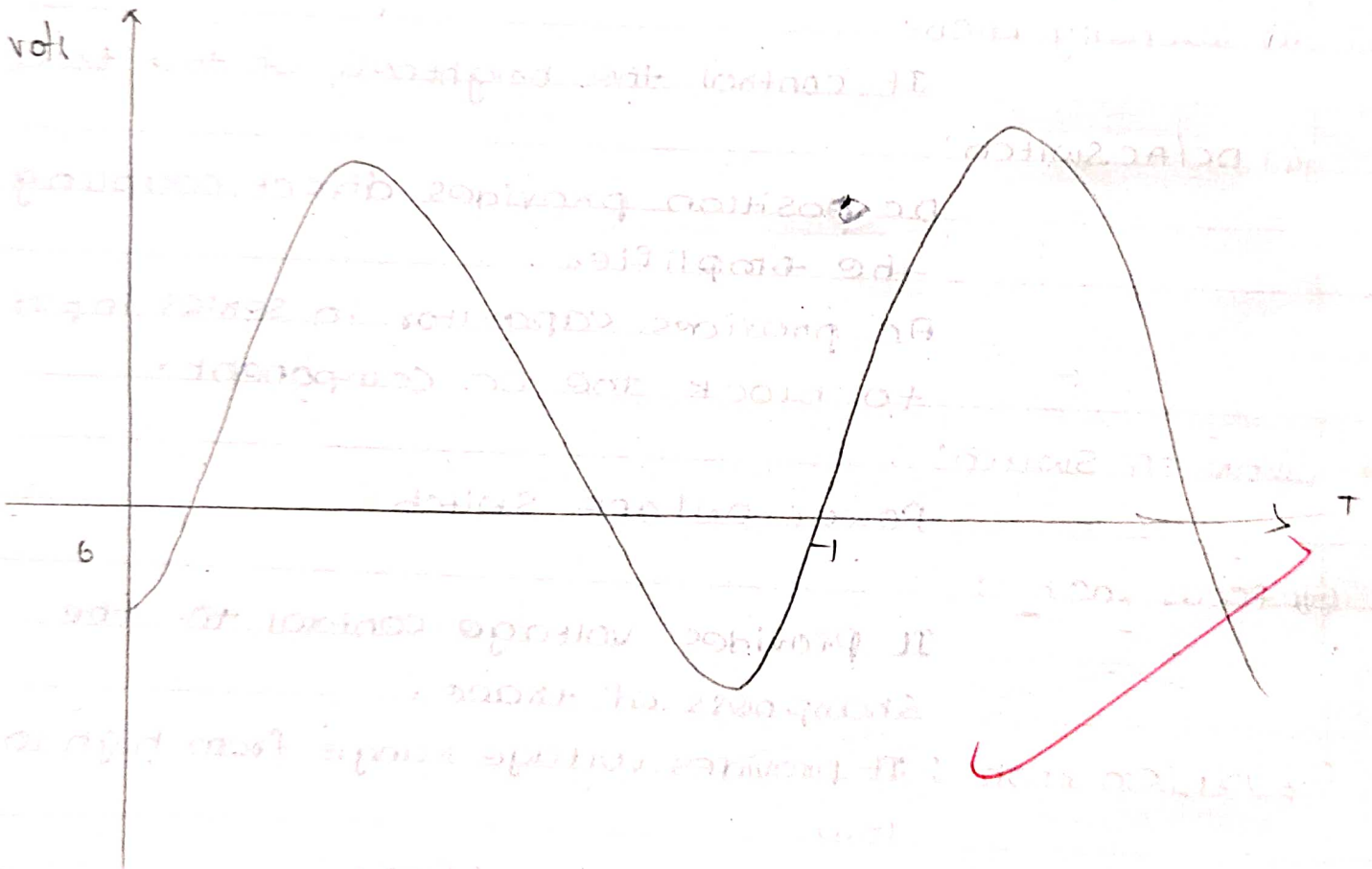
(v) volt/cm knob :- It provides voltage range from high to low

It is voltage amplifier.

(vi) Time/cm :- It is time base adjustment

It is time multiplier.

S.NO	Time period (T)	frequency [F = 1/T]	Peak to peak voltage	Amplitude (or) voltage
10	3x20s	327 Hz	5x2V	2.5x2V



(vi) Horizontal position knob:

It moves the horizontal on the CRT (x-axis)

(vii) Vertical position knob:

It moves the trace vertically (y-axis)

Procedure:

Measurement of voltage and frequency.

1. Apply a sinusoidal wave to input terminal of CRO
2. put AC/DC switch in AC position
3. Adjust volts/cm knob and time to readable display
4. By adjusting the voltage for sinusoidal signal and note the peak to peak voltage as $2V$.
5. By adjusting the time knob note the frequency to the fixed voltage.
6. tabulate the readings.

~~Failures~~

Result:-

parameters of given AC signal are calculating by using CRO.

Date: 13-9-19

Aim:-

To design a combinational circuit, to implement and verify the JK and SR FLIP FLOPS.

Apparatus Required:-

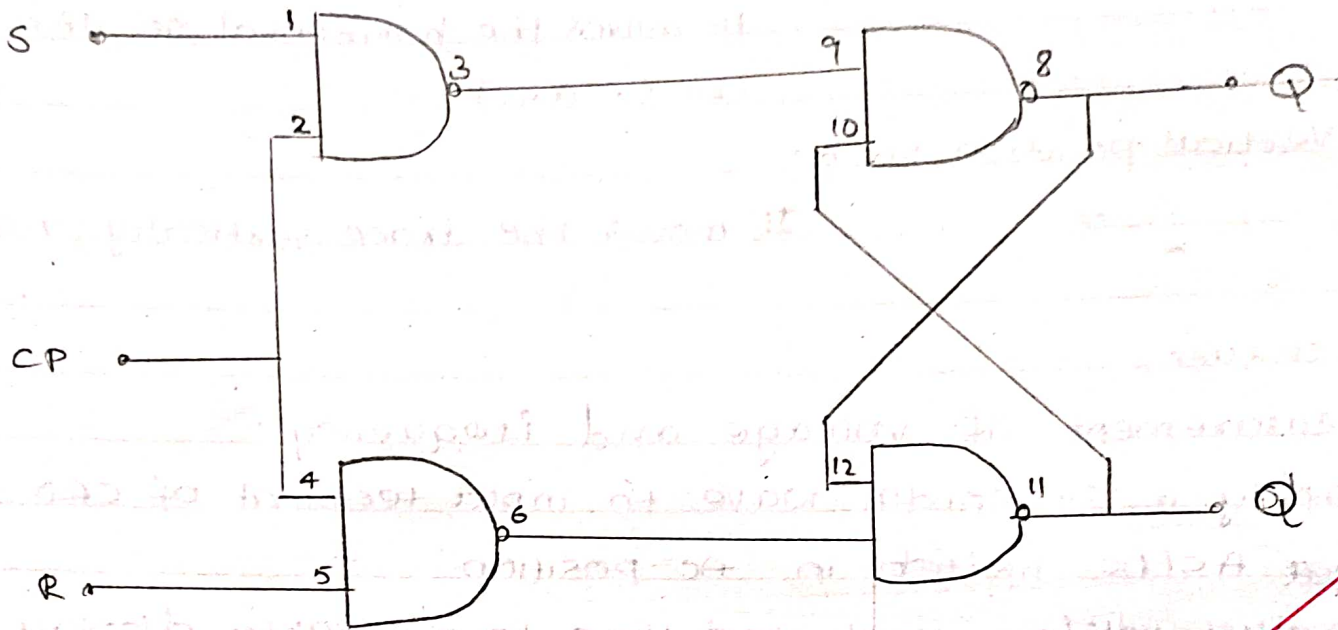
S.NO	Name of Apparatus	Range	Quantity
1.	Digital IC train kit		1
	connecting wires		As required

Theory

RS Flip Flop: The clocked RS flip flop consists of NAND gates and the output changes its state with respect to the input on application of clock pulse. When the clock pulse is high the S and R inputs reach the second level NAND gates in their complementary form. The flip flop is reset when the R inputs are high the output is in an indeterminate state.

JK Flip Flop: The indeterminate state in the SR flip-flop is defined in the JK flip flop. JK inputs behave like S and R inputs to set and reset the flip flop. The output Q is ANDed with K input and the clock pulse, similarly the output Q' is ANDed with J input and the clock pulse. When the clock pulse is zero both the AND gates are disabled and the Q and Q' output retain their previous values. When the clock pulse is high, the J and K inputs reach the NOR gates. When the both inputs are high the output

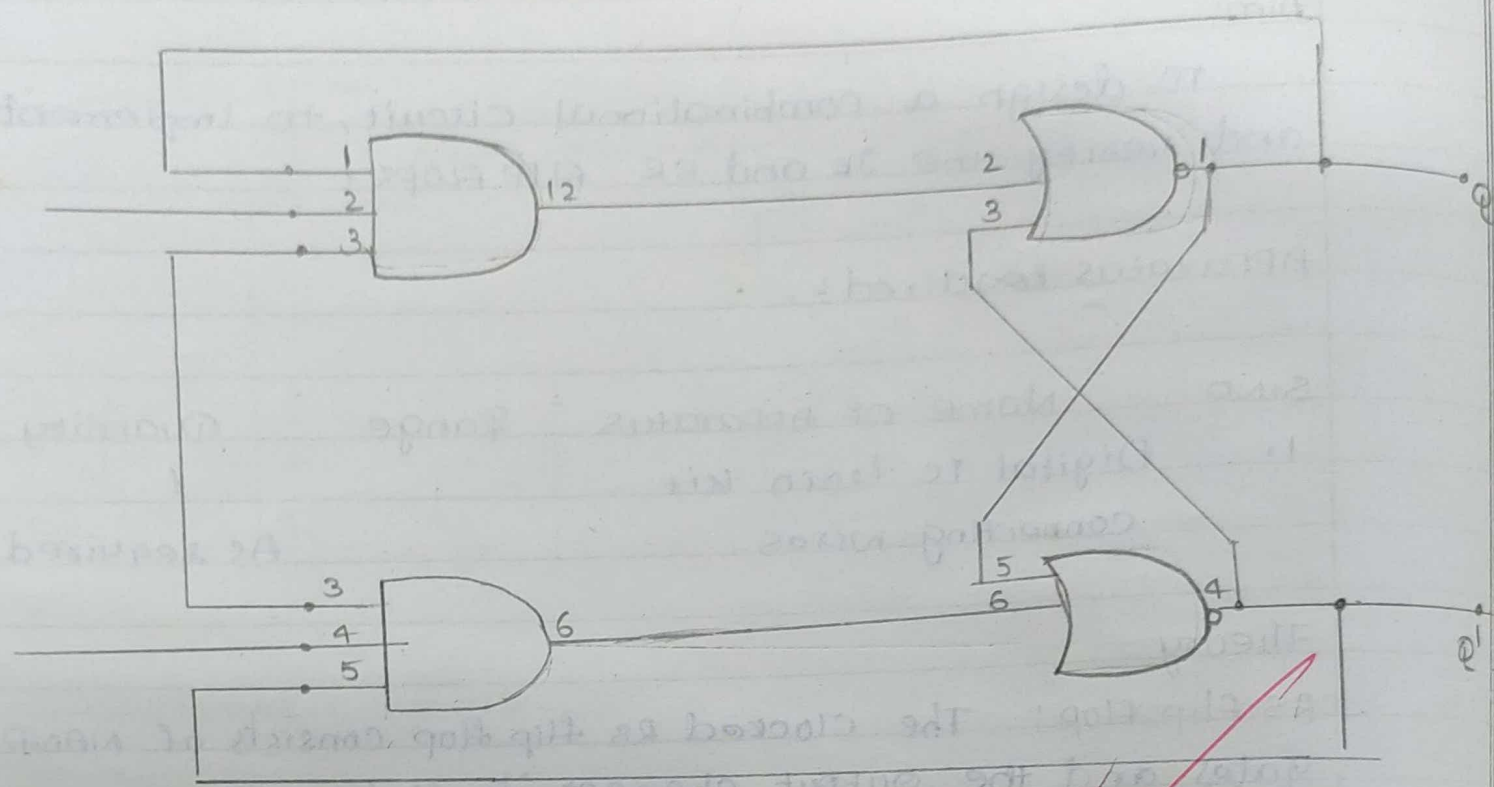
a) RS FLIP-FLOP



CHARACTERISTIC TABLE :

S.NO	INPUT		Q'	Q
	S	R		
1	0	0	0	0
2	0	0	1	1
3	0	1	0	0
4	0	1	1	0
5	1	0	0	1
6	1	0	1	1
7	1	1	0	X
8	1	1	X	X

b) JK FLIP FLOP :



CHARACTERISTIC TABLE :

S.NO	INPUT		Q'	Q
	J	K		
1	0	0	0	0
2	0	1	0	0
3	1	0	1	1
4	1	1	Q	Q
5	0	0	Q	Q
6	0	1	Q	Q
7	1	0	Q	Q
8	1	1	Q	Q

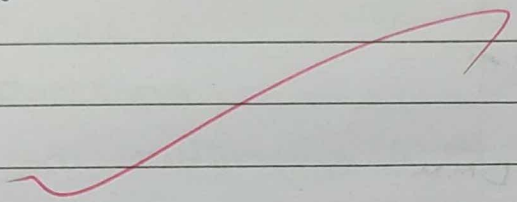
ate :

toggles continuously. This is called race around condition and this must be avoided.

Procedure :

1) FLIP-FLOPS :

1. connections are given as per the circuit diagrams.
2. For all the ICs 7th pin is grounded and 14th pins is given +5V supply.
3. Apply the inputs and observe the status of all flip flops.



~~Failures~~

~~Result :- A circuit was designed to implement the flip-flop and the output was verified~~

HALF WAVE RECTIFIER

Objective:

1. To plot input and output waveforms of the half-wave rectifier.

Apparatus:

S.No	Apparatus	Type	Range	Quantity
1.	Transformer	Step-down	0-12V	01
2.	Diode	1N4007		01
3.	Resistor		10-1k Ω	01
4.	Digital Multimeter (DMM)		(0-20V)	01
5.	CRO & CRO Probes			01
6.	Bread board & Wires			01

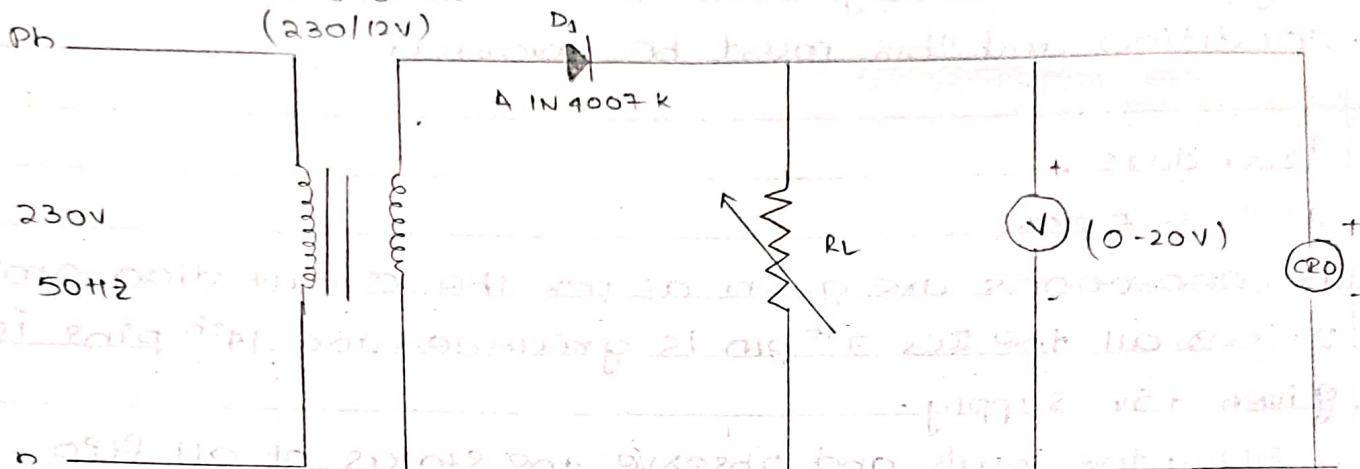
Precautions:

1. The primary and secondary sides of the transformer should be carefully identified.
2. The polarities of the diode should be carefully identified.

Procedure:

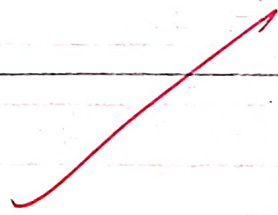
1. connections are made as per the circuit diagram of the rectifier without filter.
2. connect the primary side of the transformer to ac mains and the secondary side to the rectifier input.
3. Note down the no load voltage before applying the load to the circuit using the Multimeter, measure

STEP DOWN
TRANSFORMER
(230/12V)



Tabular column : using DMM

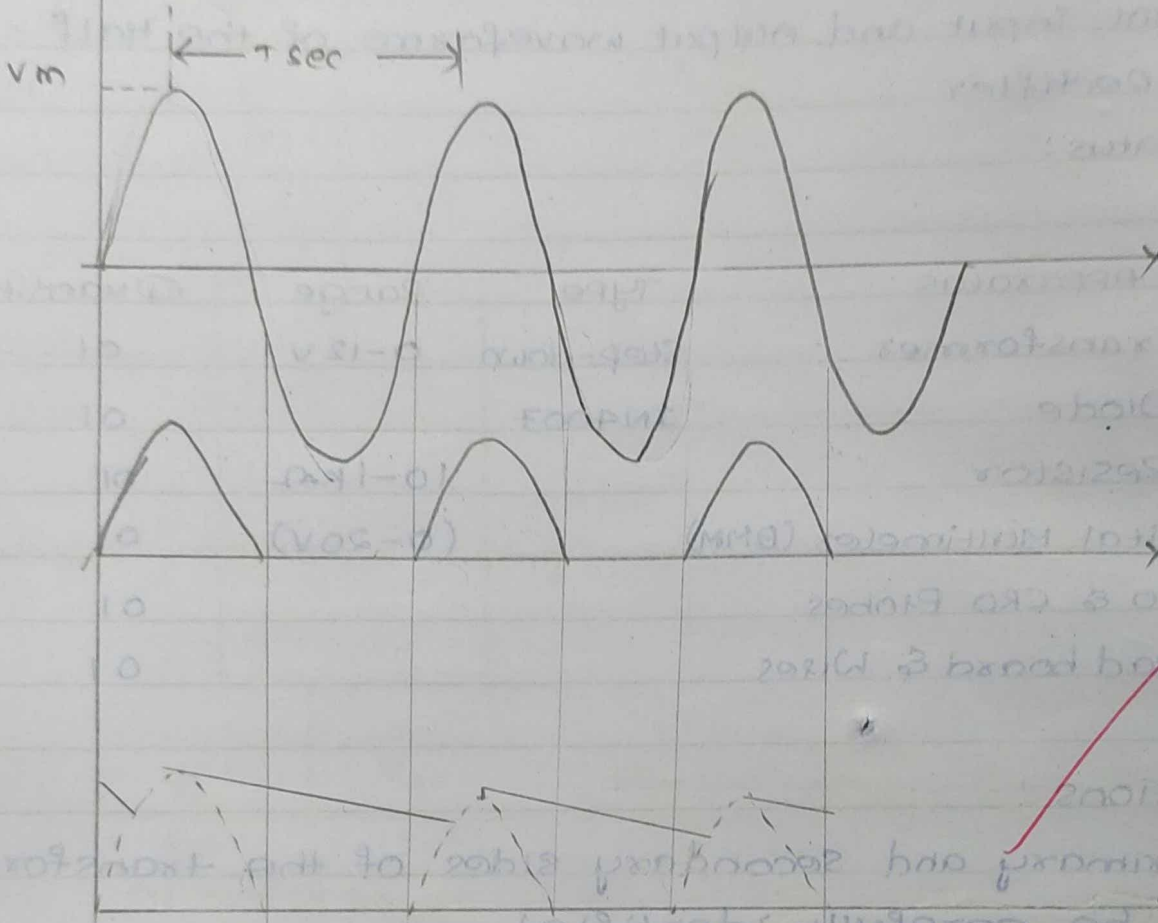
V_{ac}	V_{dc}	RIPPLE factor (γ) = V_{ac}/V_{dc}
I/P	4x5ms	6x5V
O/P MWR	2x5ms	3x5V
O/P SUR	2x5ms	3x5V



A circuit was designed to measure the ripple factor. The circuit was a half-wave rectifier with a load resistor and a capacitor. The ripple factor was measured using a DMM.

OUTPUT WAVEFORMS ;

Amplitude (V)



Procedure:

1. connections are made as per the circuit diagram of the rectifier without filter.
2. Connect the primary side of the transformer to ac mains and the secondary side to the rectifier input.
3. Note down the no load voltage before applying the load to the circuit using the multimeter measure.

the ac input voltage of the rectifier and its frequency

4. Now vary the R_L in steps of $100\ \Omega$ by varying the DRB from $1100\ \Omega$ to $100\ \Omega$ and note down the load voltage (V_L) using the multimeter of each value of R_L and calculate the percentage regulation.
5. Measure the AC and DC voltage at the output of the rectifier for each value of R_L using Multimeter
6. Now observe the output waveform on CRO across R_L and find out value of V_m .
7. Now calculate V_{dc} , V_{rms} , Ripple factor and other parameters of half wave rectifier according to the given formulae.
8. Measure the amplitude and time period of the transformer secondary (input waveform) by connecting CRO.
9. Feed the rectified output voltage to the CRO and measure the time period and amplitude of the waveform.

FULL WAVE RECTIFIER

Objective:

1. To plot input and output waveforms of the full wave rectifier with and without filter.
2. To find ripple factor for full wave rectifier with and without filter.
3. To find regulation for full wave rectifier with and without filter.

Apparatus:

S.NO	Apparatus	Type	Range	Quantity
	Transformer	Center tapped	12-0-12V	01
	Diode			02
	Resistance		1k Ω	01
	Multimeter		(0-20V)	01
	CRO			01
	Bread board and wires			01

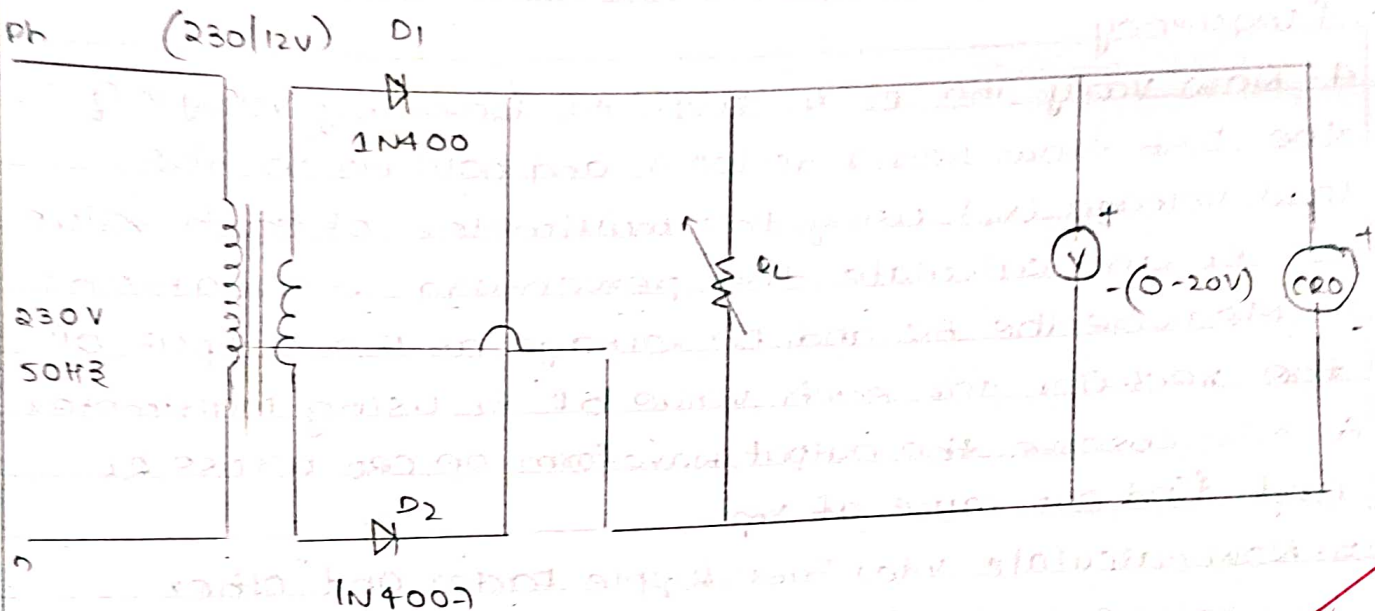
Precautions:

1. The primary and secondary sides of transformer should be carefully identified.
2. The polarities of the diode should be carefully identified.

Procedure:

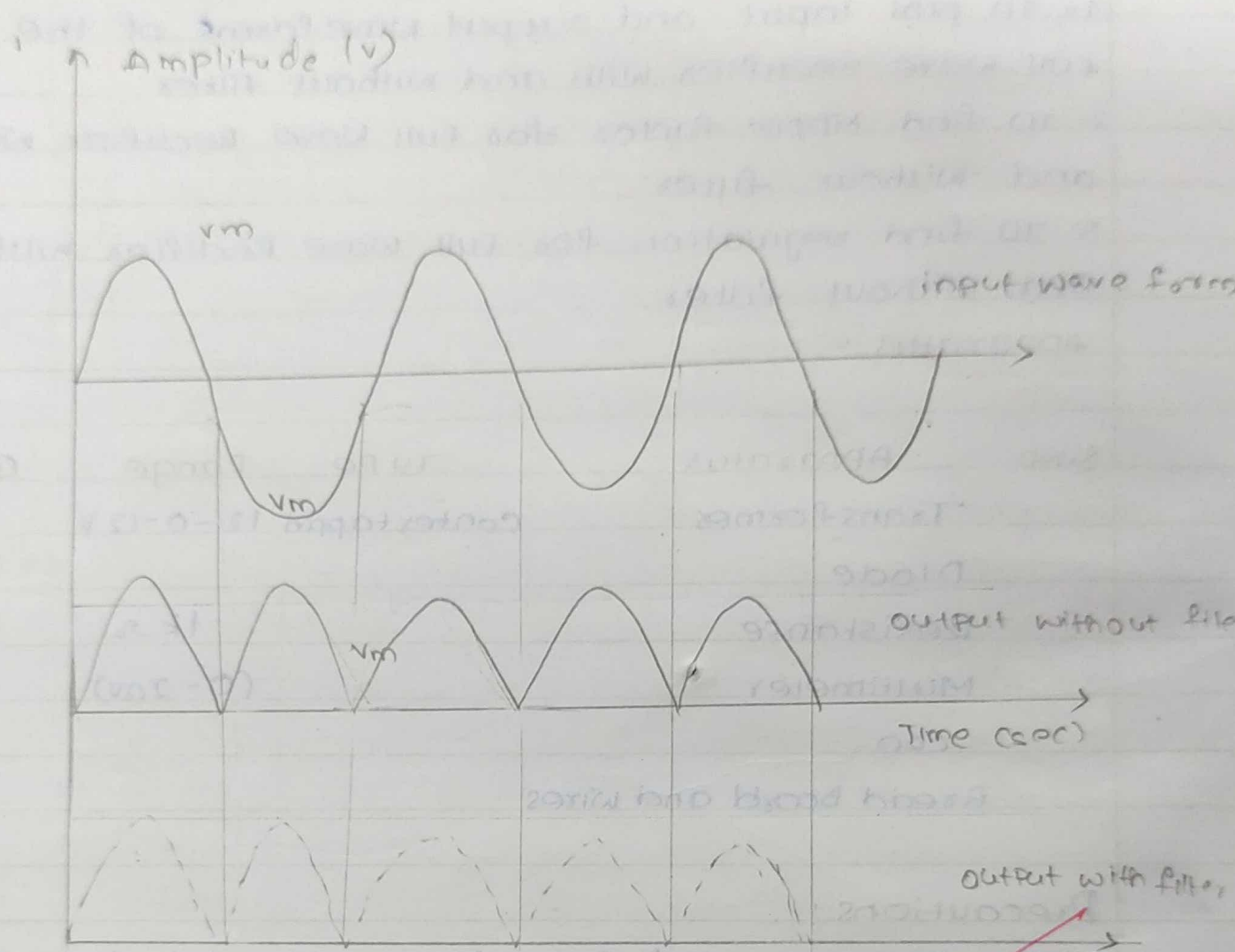
1. Connections are made as per the circuit diagram of the rectifier without filter.
2. Connect the primary side of the transformer to AC mains and secondary side to the rectifier input.

Circuit diagram



Tabular column:

Wave form Signal	Time	Amplitude
V_{in}	$2 \times 10\text{ms} = 20\text{ms}$	$2 \times 20\text{V} = 40\text{V}$
V_{out}	$1 \times 10\text{ms} = 20\text{ms}$	$2 \times 10\text{V} = 20\text{V}$
V wt PWR	10ms	20V



output with filter

Date :

3. By the multimeter, measure the ac input voltage of the rectifier and, ac and dc voltage at the output of the rectifier.

4. Measure the amplitude and time period of the transformer secondary (input waveform) by connecting CRO.

5. Feed the rectified output voltage to the CRO and measure the time period and amplitude of the wave form.

~~Failures~~
Result: the input and output wave forms of full wave rectifier is plotted.

-Aim: To demonstrate the use of op-amps.

1. Summing amplifier
2. Subtractor
3. Integrator
4. Differentiator

-Apparatus required:

Sl. No	Apparatus	Type	Range	Quantity
1.	Op-AMP	741		1
2.	Resistors		10K/1K	1
3.	Signal Generator			1
4.	CRO			1
5.	Dual power supply			1
6.	Bread Board			1
7.	Connecting wires			1

Theory:

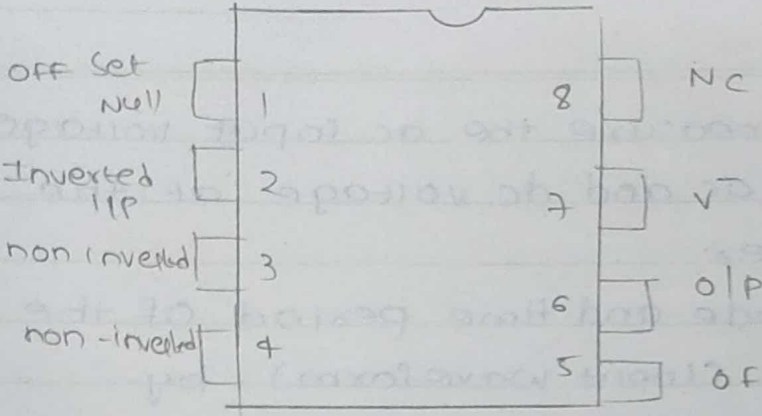
Summing Amplifier: op-amp may be used to perform summing operation of several input signals in inverting and non-inverting mode. The input signals to be summed up are giving to inverting terminal or non-inverting terminal through the input resistance to perform inverting and non-inverting summing operations respectively.

Procedure:

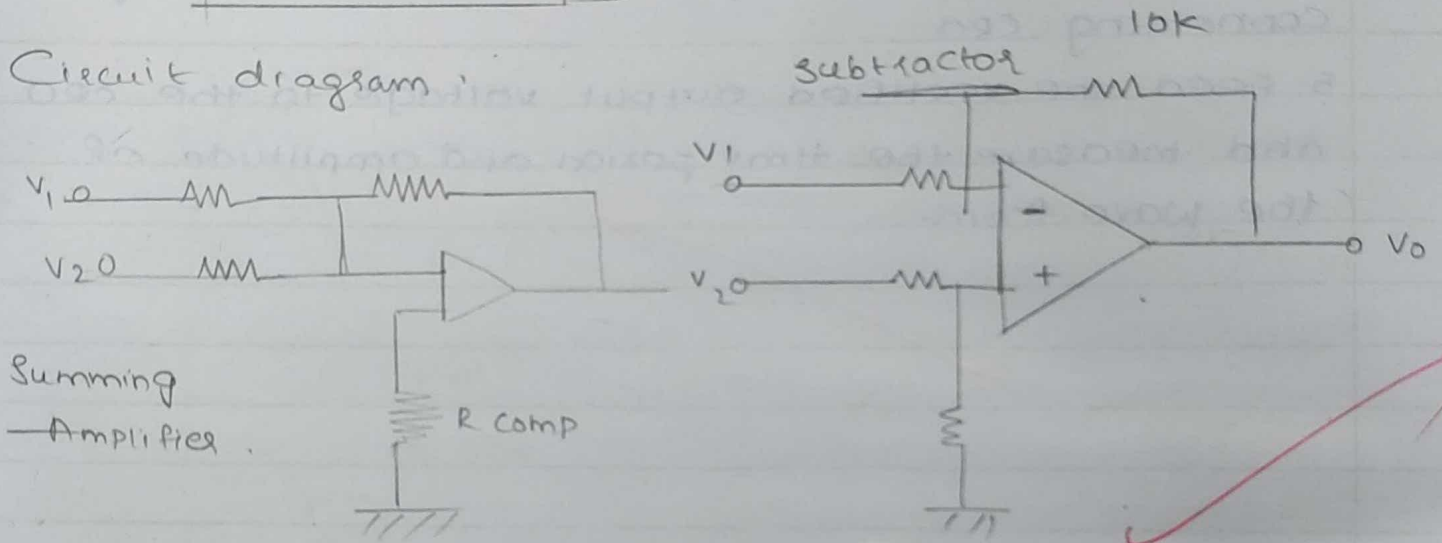
a) Inverting - summing amplifier:

1. Connect the circuit as shown in figure.

PIN DIAGRAM



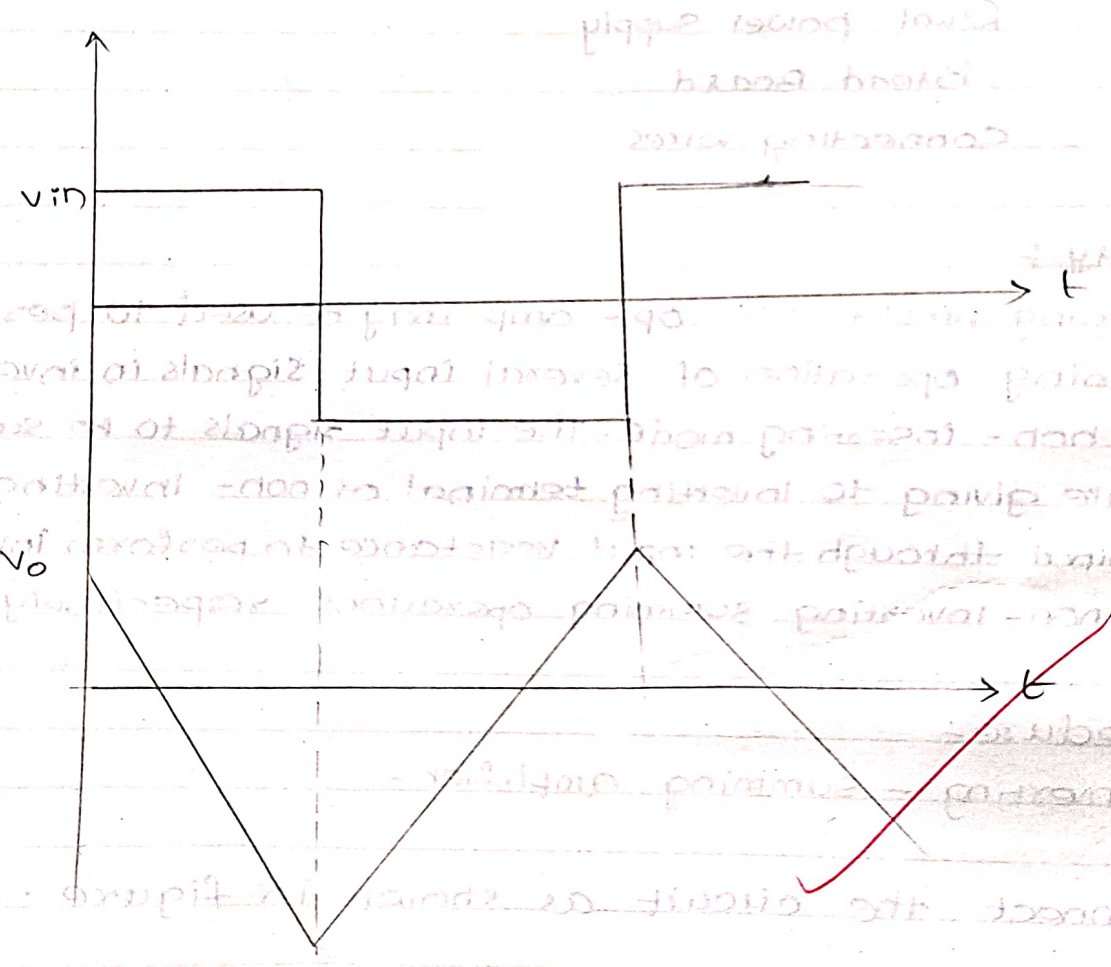
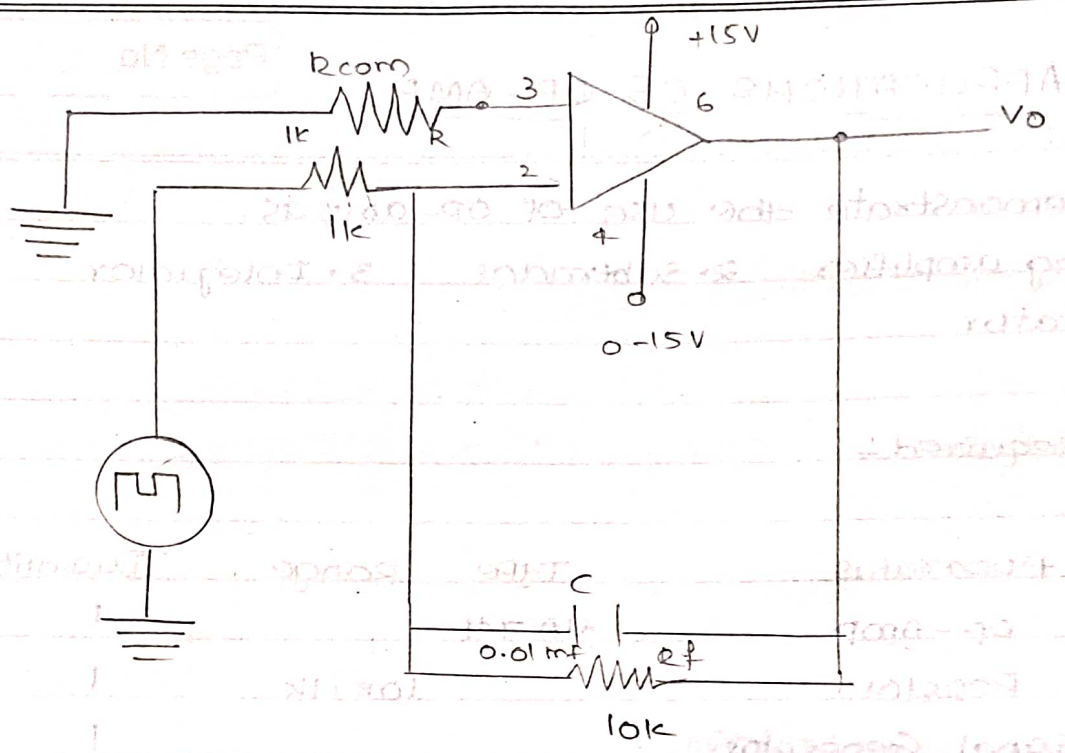
Circuit diagram:



S.NO	V1	V2	Theoretical $V_o = V_1 - V_2$	Practical V_o
1	5	34	8.4	8.5
2	10	10	20	21.8

S.NO	V1	V2	Theoretical $V_o = V_1 + V_2$	Practical V_o
1	10	7	3	3.003
2	10	12	-2	-2.01

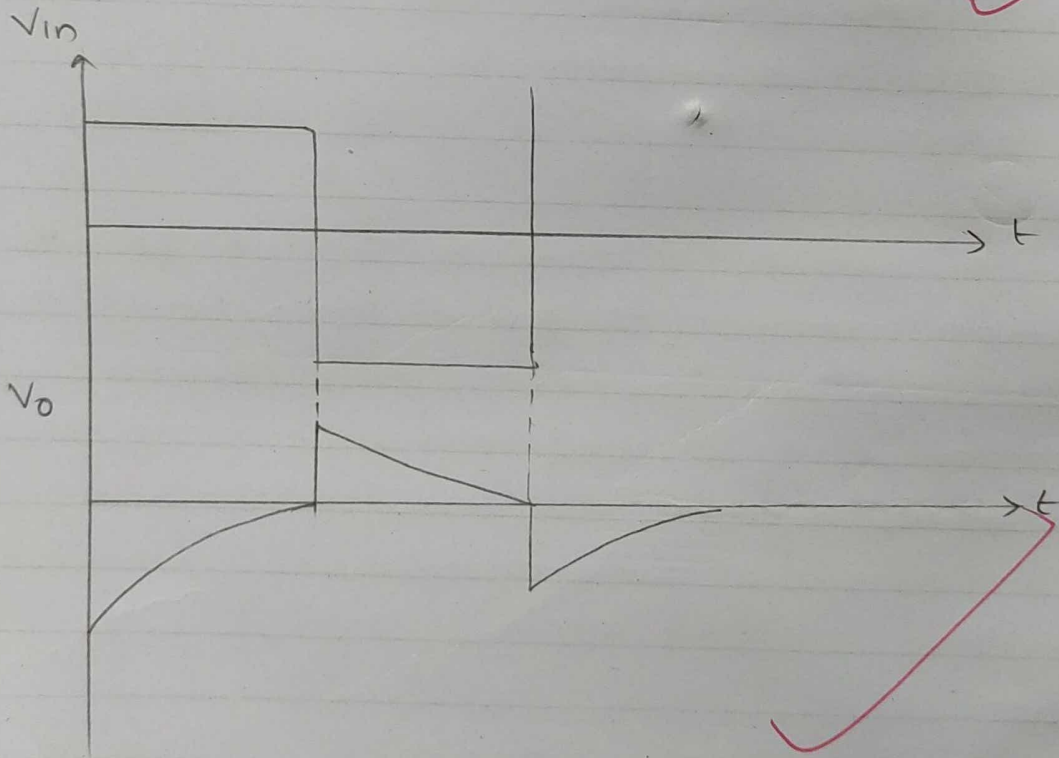
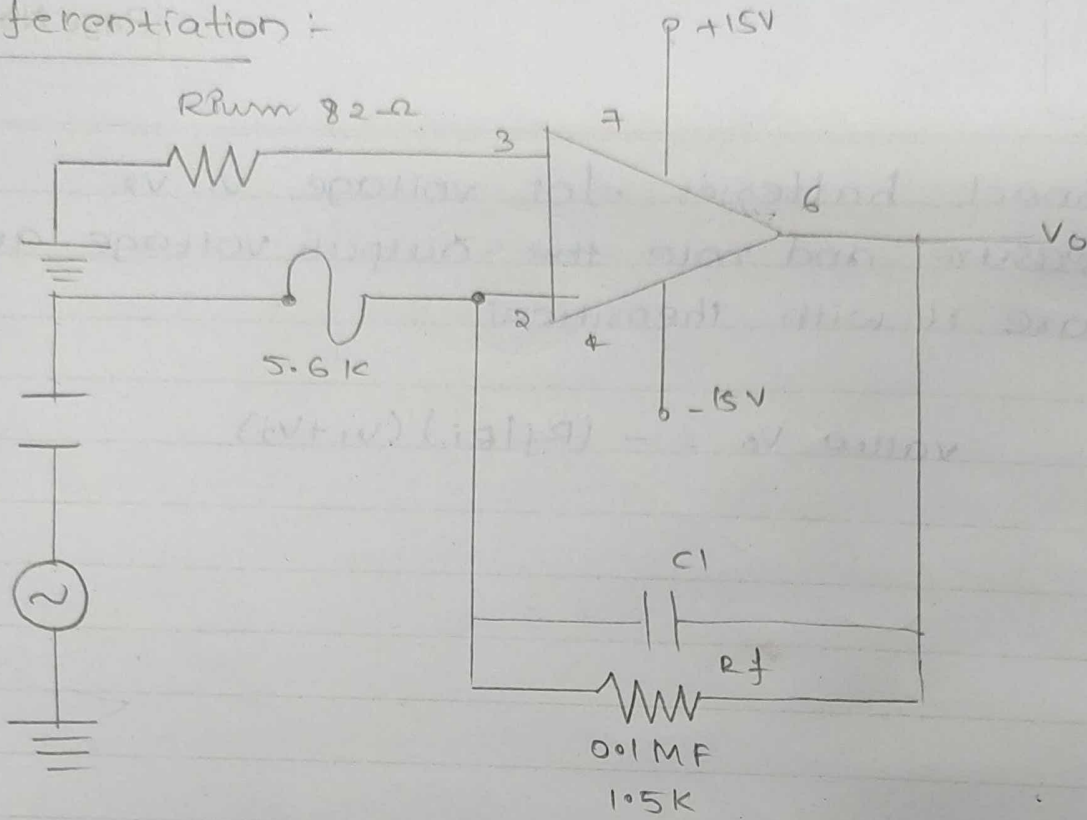
Page No.
 Date: 15/10/17



- 2) connect batteries for voltage V_1, V_2
- 3) Measure and note the output voltage and compare it with theoretical

$$\text{value } V_0 = - \left(\frac{R_f}{R_i} \right) (V_1 + V_2)$$

Differentiation :-



Expt. No.

Page No. 14

Date :

Hair

Result :-

Thus, the use of op-amp as summing amplifier, subtractor, voltage comparator, zero crossing detector, integrator, differentiator was studied.

Date: 8-11-19

CONFIGURATION

Aim: TO study and Plot the transistor characteristics in CB configuration.

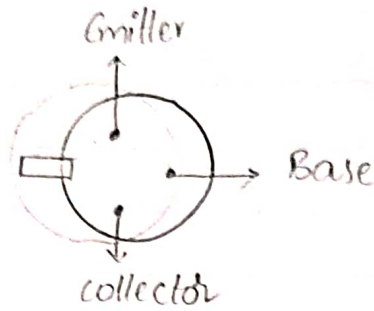
Apparatus required:

S. NO	Name	Range	Quantity
1.	RPS	(0-30)V	2
2.	Transistor	BC-107	1
3.	resistor	10, 1K- Ω	1
4.	Bread Board	(-)	1
5.	Ammeter	(0-10) mA (0-1)A	1, 1
6.	voltmeter	(0-30)V, (0-2) V	1, 1

Theory:-

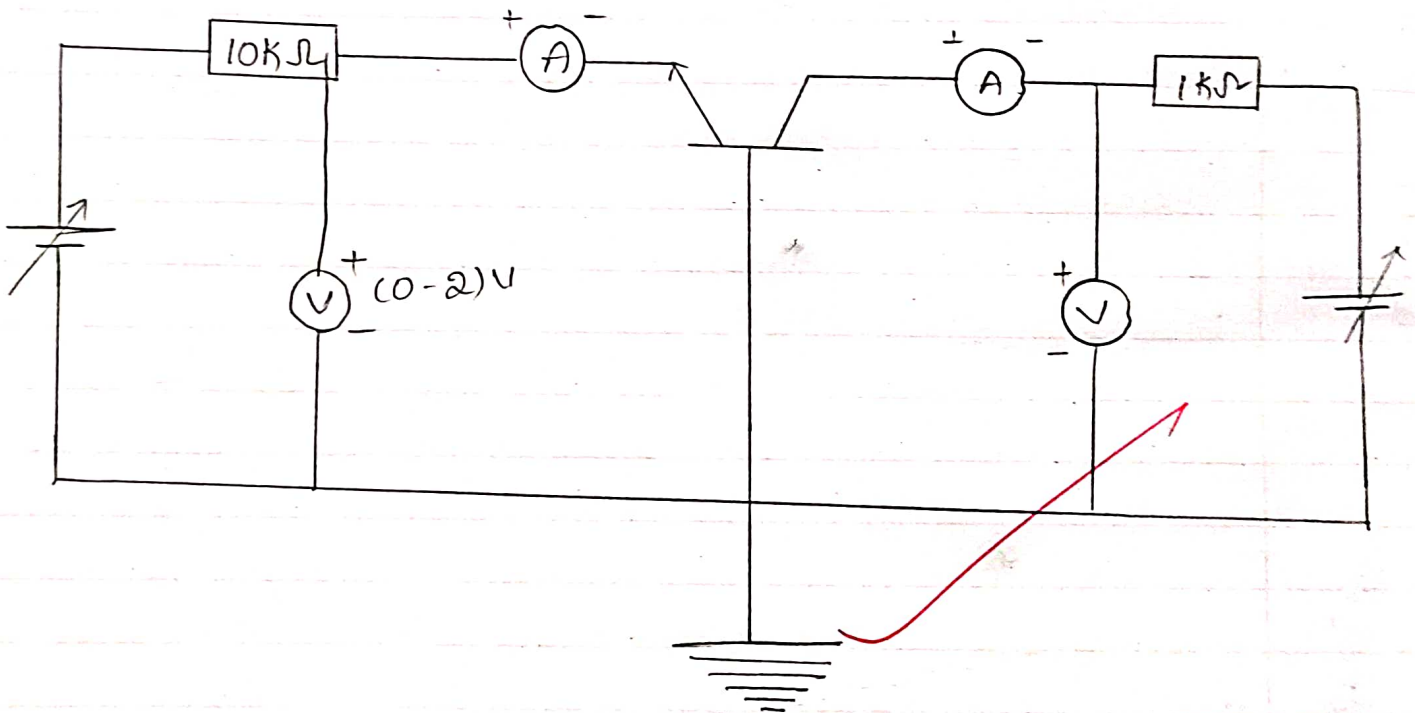
In this configuration the base is made common to both the input and out. The emitter is given the input and output is taken across the collector. The current gain of this configuration is less than unity. The voltage gain of CB configuration is high. Due to the high voltage gain, the power gain also high. Due to the high β . In CB configuration the input characteristics relate I_E and V_{EB} for a constant V_{CB} . Initially let $V_{CB} = 0$ then the input junction is equivalent to a forward biased diode and the characteristics resembles that of a diode where $V_{EB} = +V_1$ (volts) due to early effect I_E increases and so the characteristics shift to the left. The output characteristics relate I_C and V_{CB}

Pin diagram



Speciation:- BC 107 / 50V / 0.1A , 0.3W , 300MHz.

Circuit Diagram:

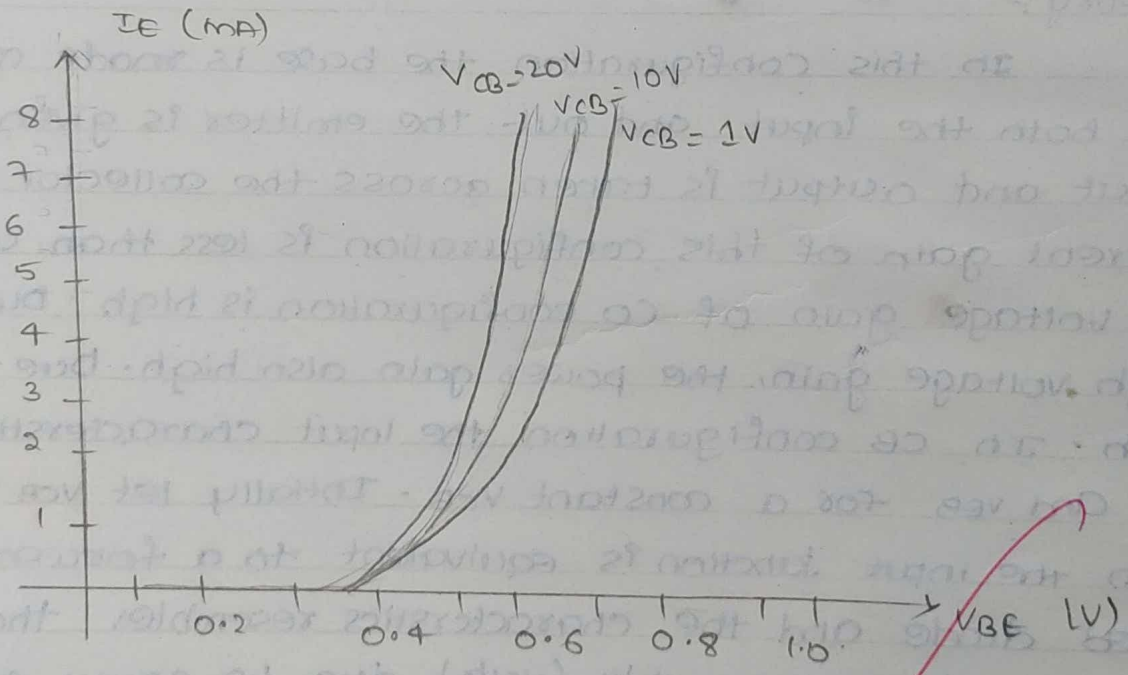


There is the use of dc supply as shown in the circuit. The voltmeter and ammeter are used to measure the voltage and current respectively. The voltmeter is connected in parallel across the resistor and the ammeter is connected in series with the resistor.

Tabular form: Input characteristics

S.no	$V_{CB} = V$		$V_{CB} = V$		$V_{CB} = V$	
	V_{EB} (V)	I_E (mA)	V_{EB} (V)	I_E (mA)	V_{EB} (V)	I_C (mA)
1.	0.01	0	0.2	0	0.2	0
2.	0.3	0	0.3	0	0.3	0
3.	0.4	0	0.4	0	0.4	0
4.	0.5	0	0.5	0	0.5	0
5.	0.6	0.2	0.6	0.2	0.6	0.2

Model Graph Input characteristics



for a constant. I_c Initially I_c increases and then it leaves for a value. $I_c = \alpha I_E$ when I_E is increased I_c also increased I_c also increases proportionally. Though increase in V_{CB} causes an increase in α , since α is a fraction it is negligible and so I_c remain a constant for all values of V_{CB} once it leaves off.

Procedure:

Input characteristics:-

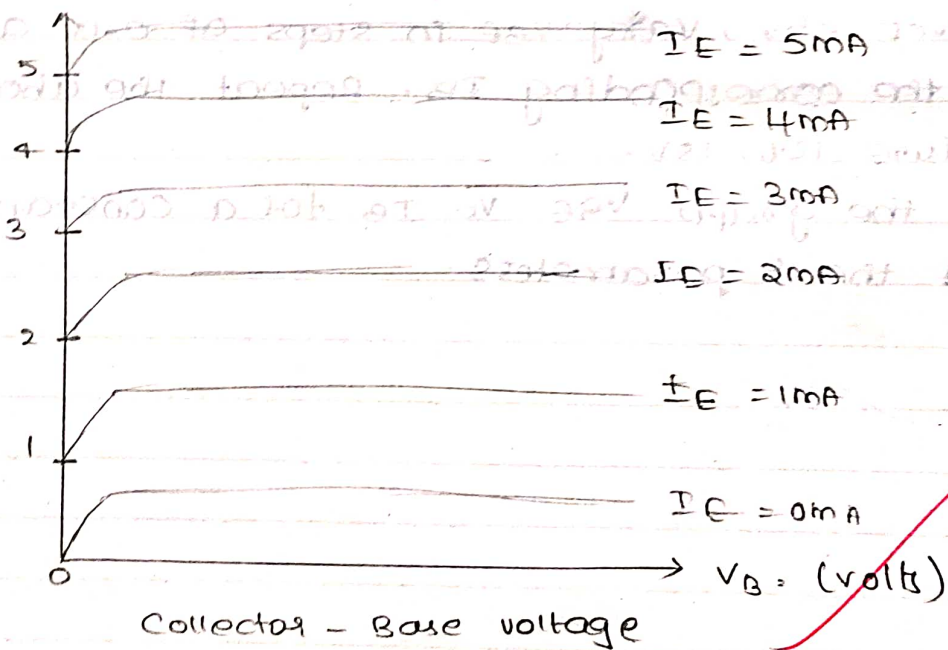
It is the curve between emitter current I_E and emitter-base voltage V_{BE} at constant collector base voltage V_{CB}

1. connect the circuit as per the circuit diagram
2. set $V_{CC} = 5V$, vary V_{BE} in steps of $0.1V$ and note down the corresponding I_B . Repeat the above procedure $10V, 15V$.
3. plot the graph V_{BE} vs I_B for a constant V_{CE} .
4. find the β parameters.

output characteristics

Q-1	$I_E = 0 \text{ mA}$		$I_E = 1 \text{ mA}$		$I_E = 2 \text{ mA}$	
	V_{CE} (V)	I_C (mA)	V_{CE} (V)	I_C (mA)	V_{CE} (V)	I_C (mA)
1.	0	0	0	0	0	0
2.	0.2	4.9	0.2	3.9	0.2	2.9
3.	0.4	5	0.4	4	0.4	3
4.	0.6	5	0.6	4	0.6	3
5.	0.8	5	0.8	4	0.8	3
6.	1.0	5	1.0	4	1.0	3

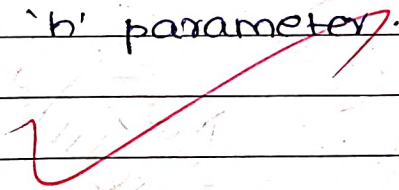
Model Graph : output - characteristics



output characteristics :-

It is the curve between collector current I_c and collector-base voltage V_{CB} at constant emitter current I_e .

- 1) connect the circuit as per the circuit diagram
2. set $I_B = 20\mu A$, vary V_{CE} in steps of 1V and note down the corresponding I_c . Repeat the above procedure for $40\mu A$, $60\mu A$, etc.
3. plot the graph V_{CE} vs I_c for a constant I_B
4. find the 'h' parameter.



4/1/2018

Result:

The transistor characteristics of a common base configuration were plotted and studied.

Current characteristics

It is the curve between collector current I_C and collector-base voltage V_{CB} at constant emitter current I_E .

1. Connect the circuit as per the circuit diagram.

2. Set the base current I_B to the value of I_E and note down the corresponding I_C . Repeat the above procedure for $20\mu A, 40\mu A, 80\mu A$, etc.

3. Plot the graph V_{CB} vs I_C for a constant I_E .

4. Find the β parameter.

Completed

Hailuwa
16/11/19

Result: The transfer characteristics of a common base configuration are plotted and studied.